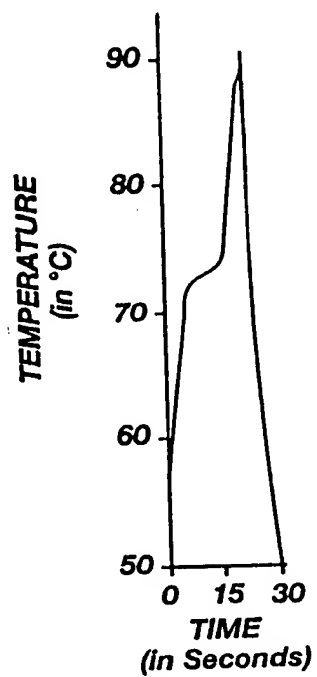


**Fig. 4**



**Fig. 5**

### Effect of Annealing Time on Product Yield and Background Amplification

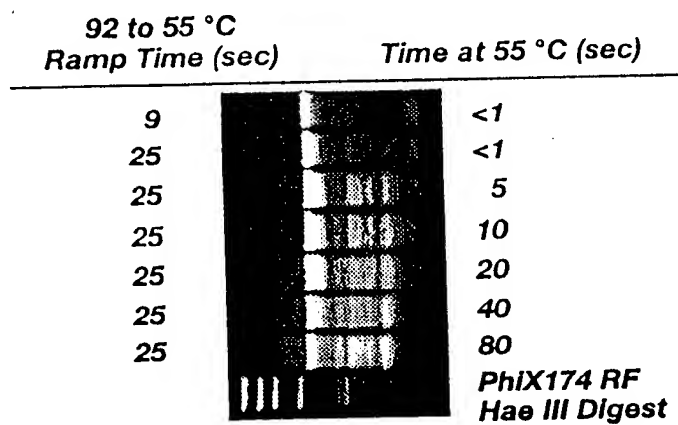


Fig. 7

### Effect of Denaturation Time on Product Yield

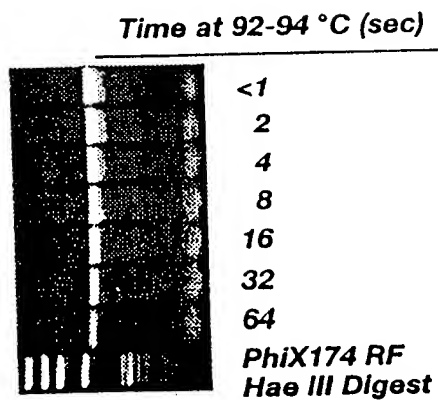
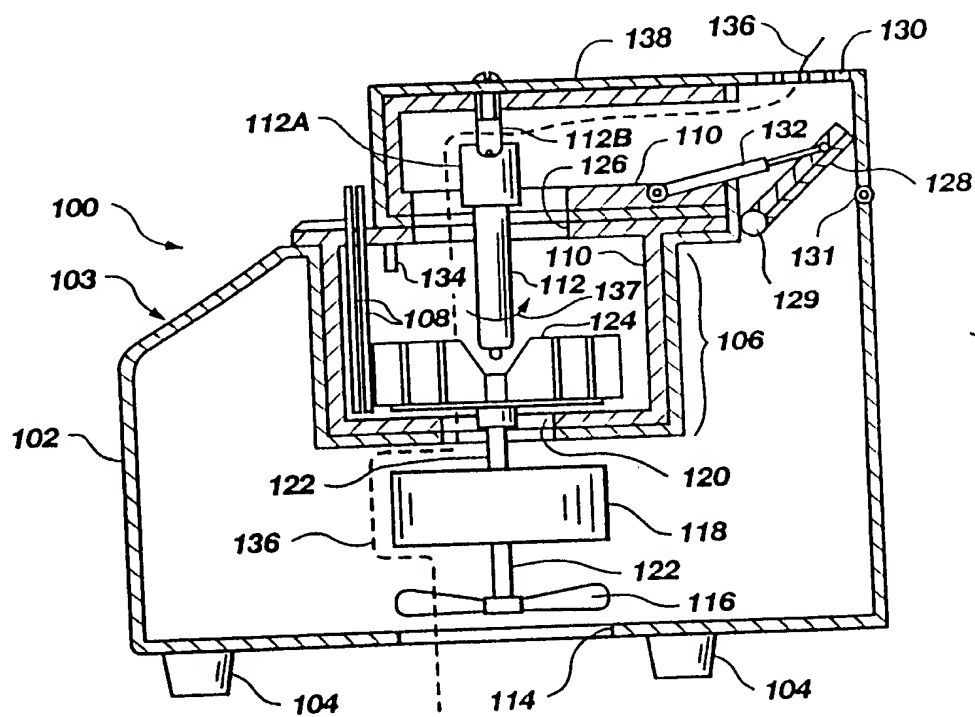
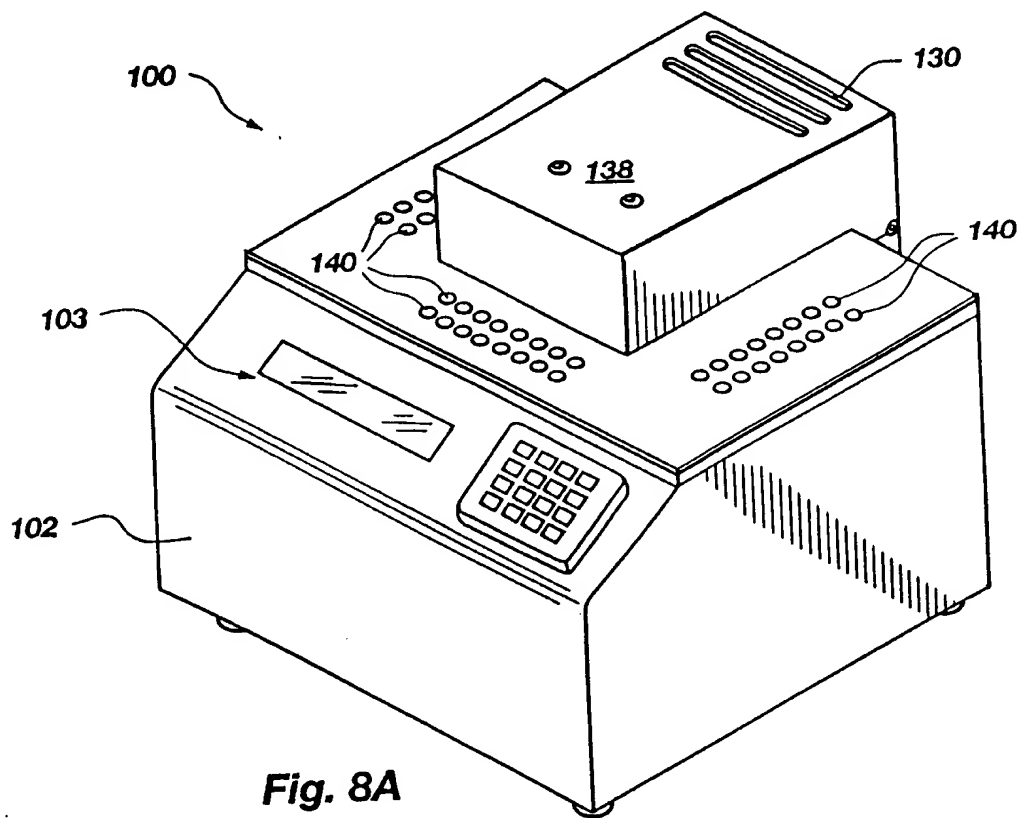
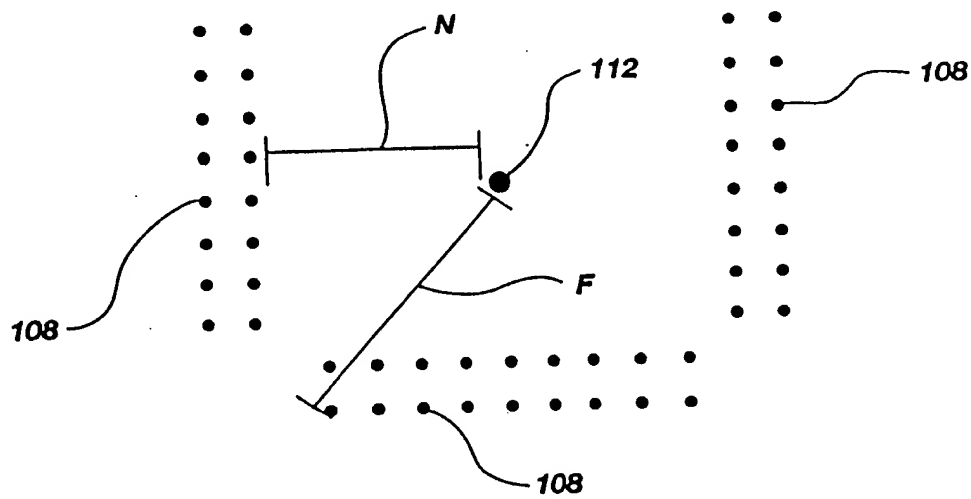


Fig. 6





**Fig. 8C**

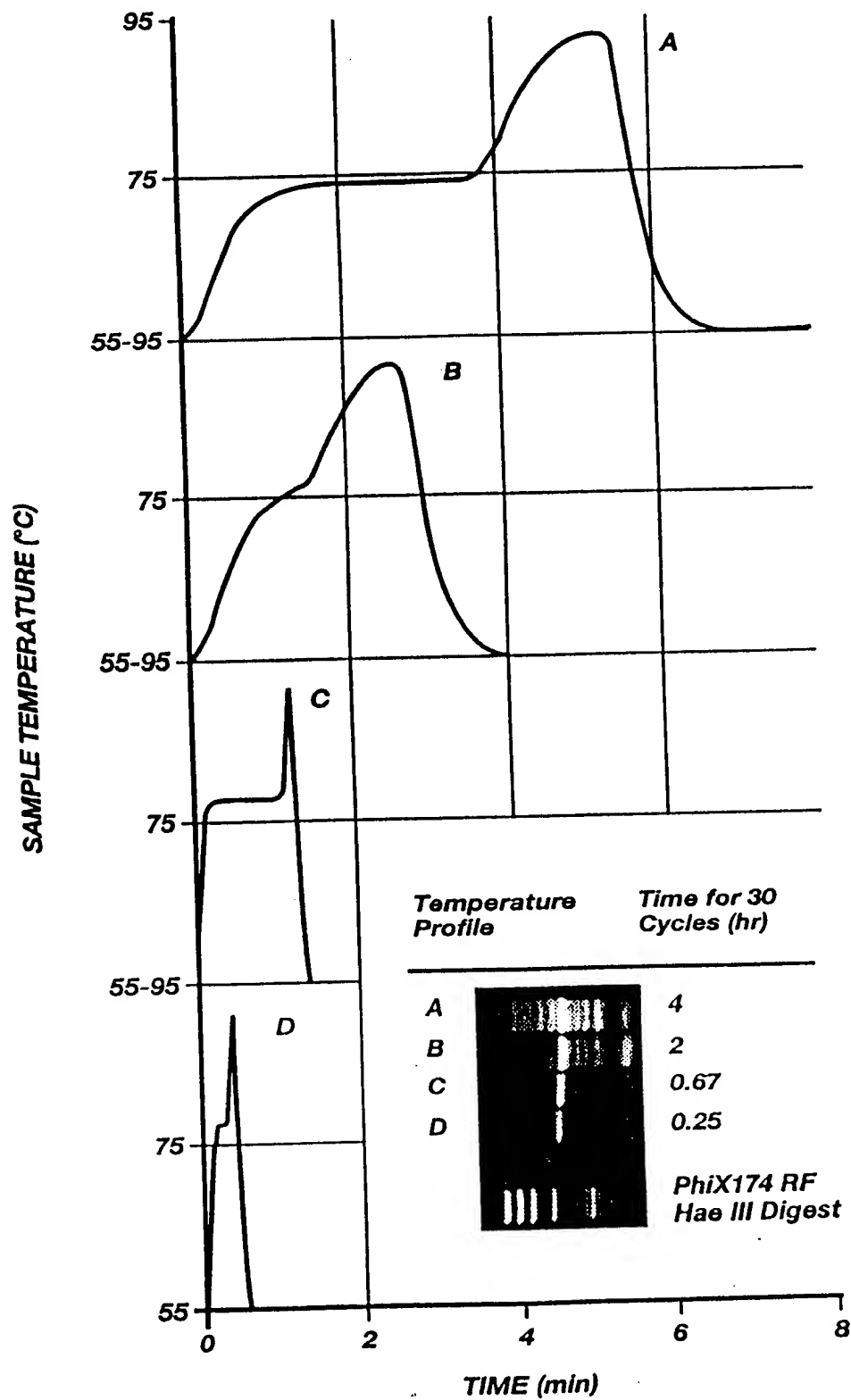
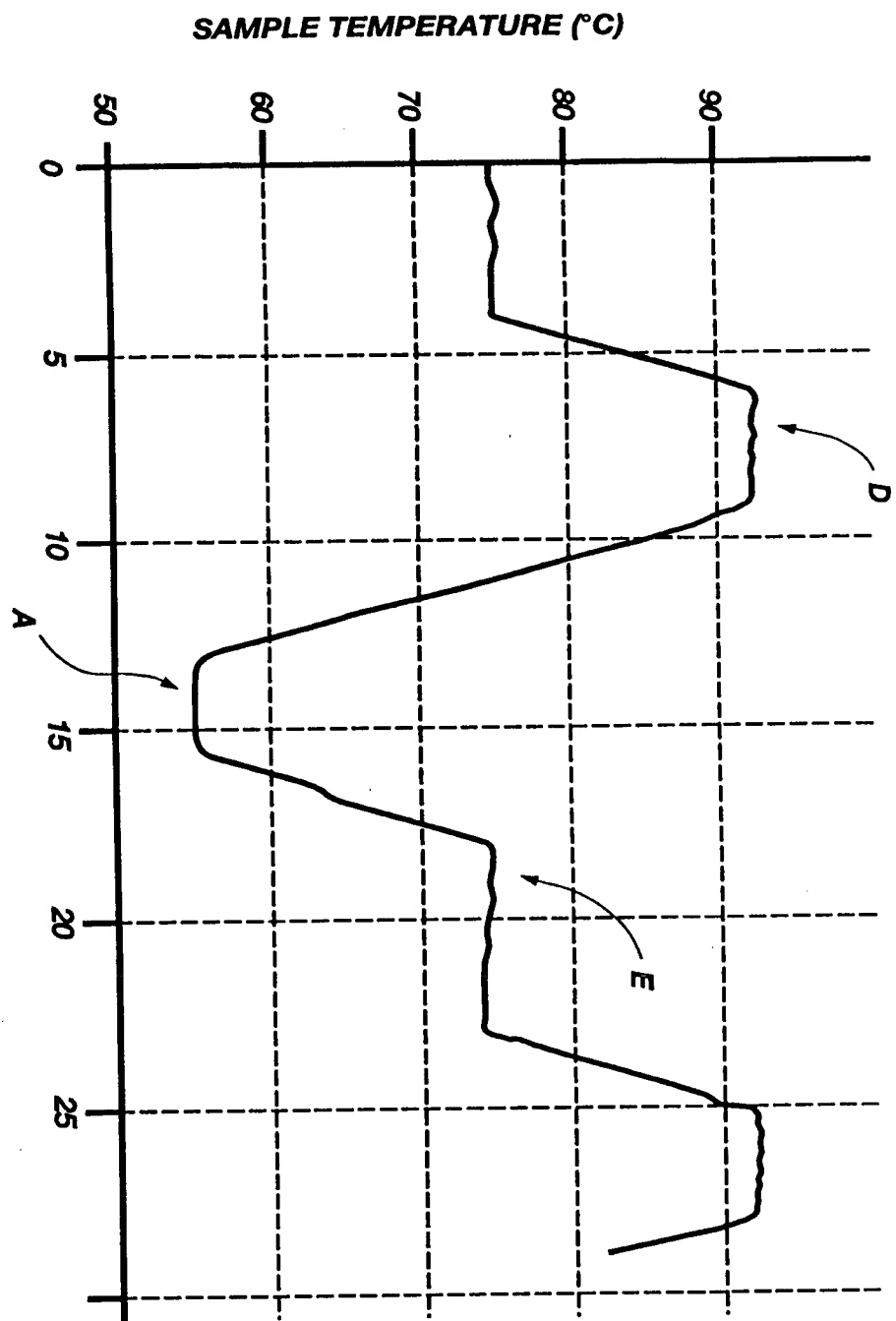
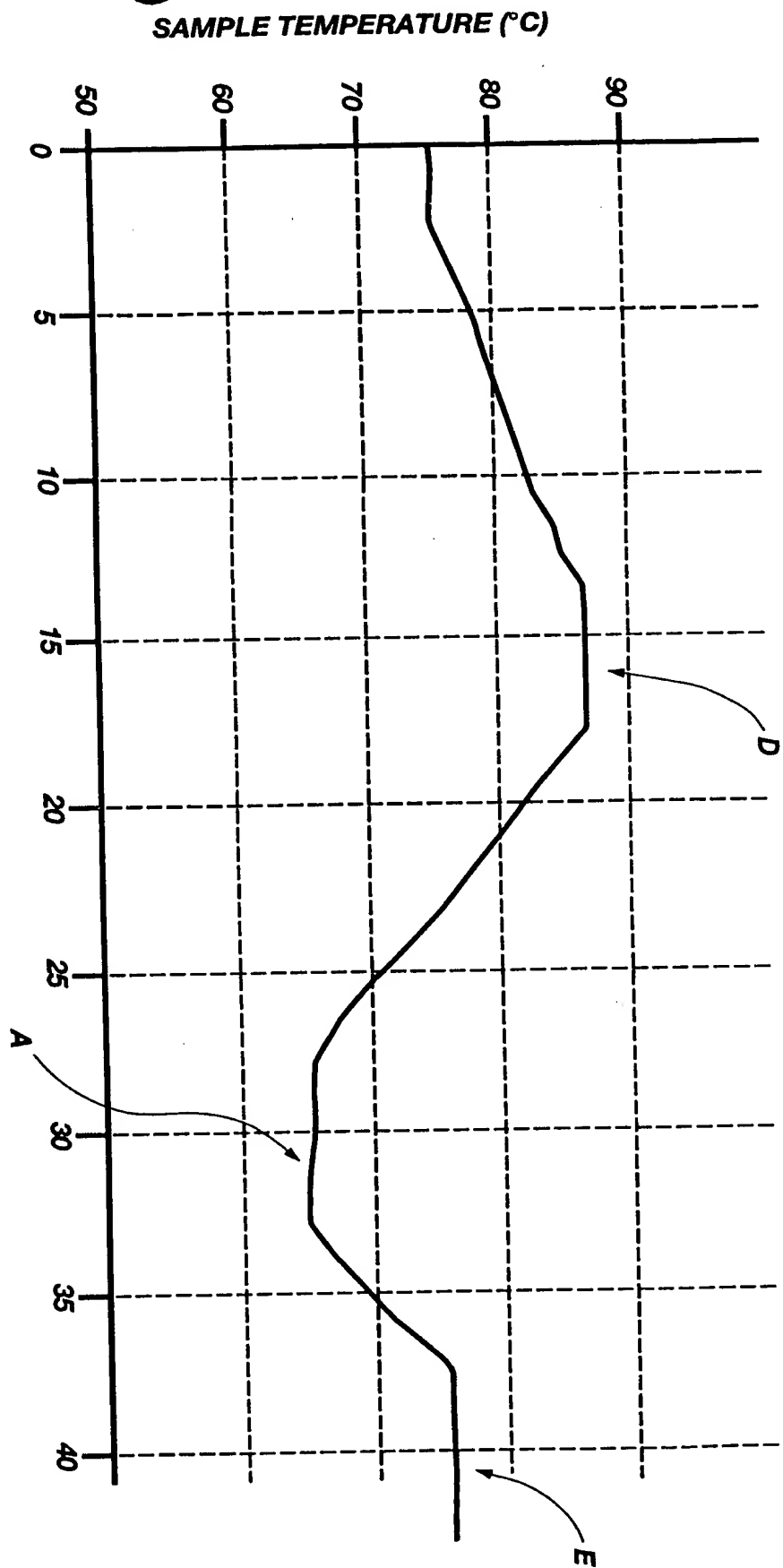


Fig. 9A



**Fig. 9B**



**Fig. 9C**



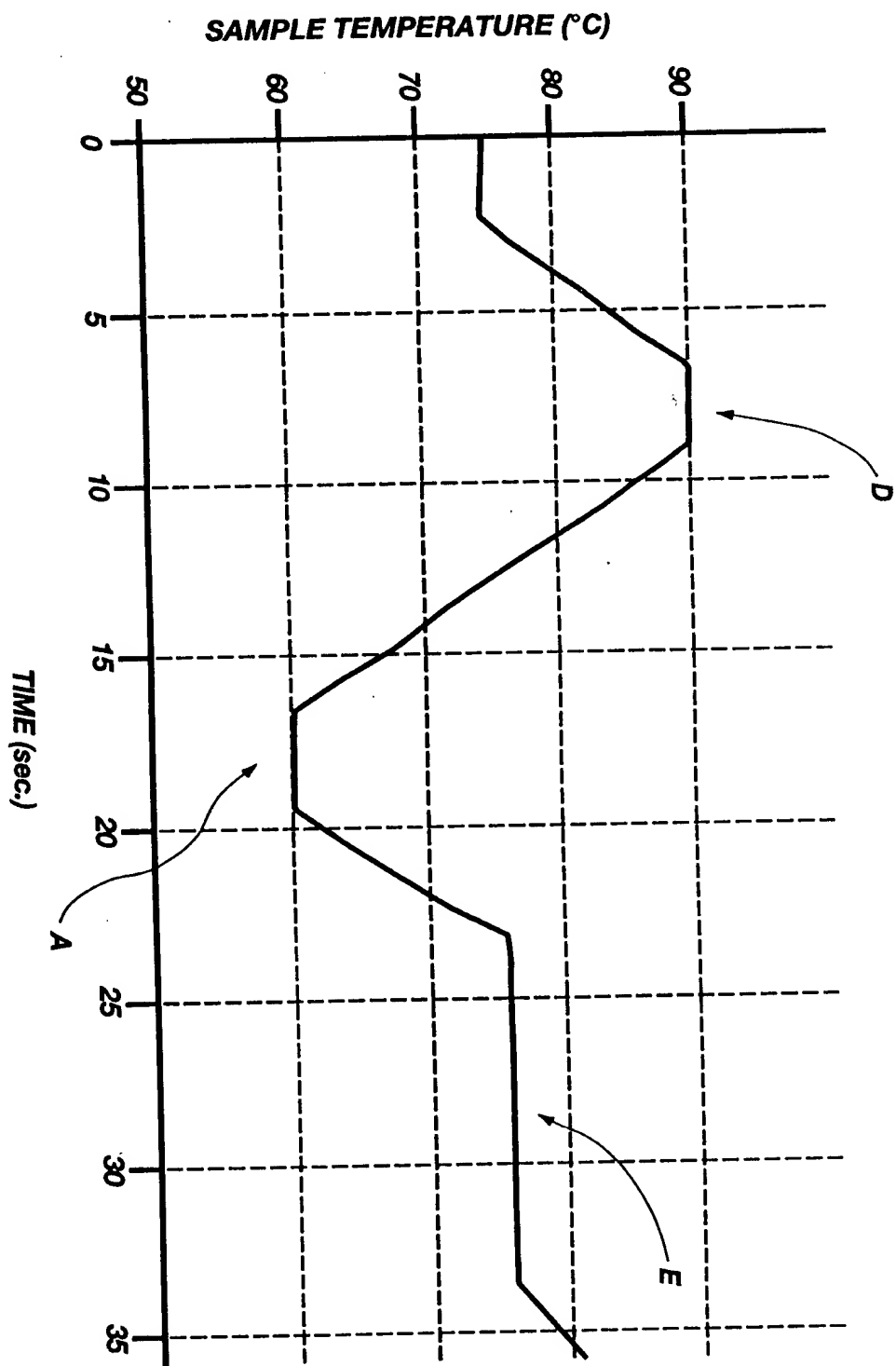
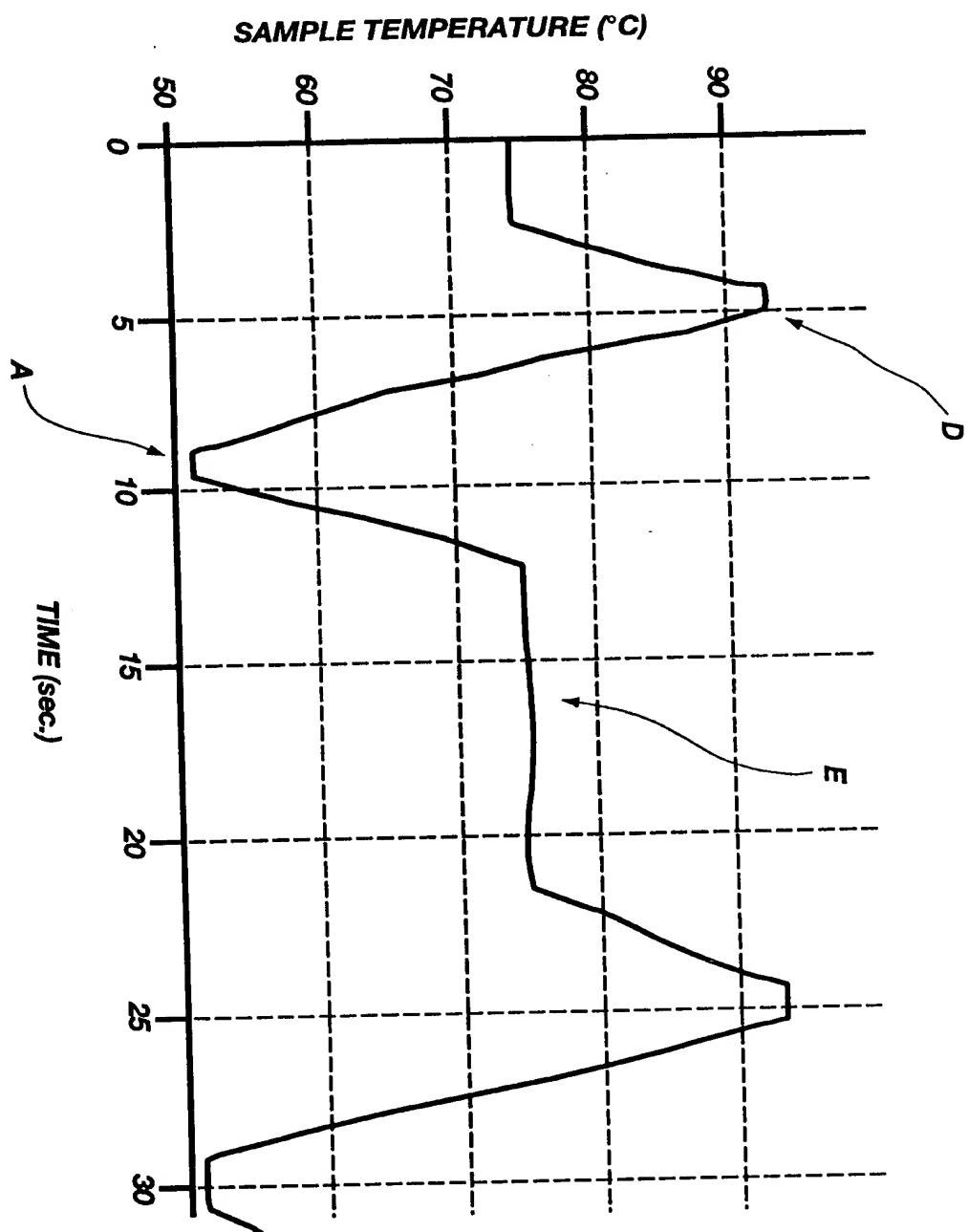


Fig. 9D

00009275-000497



**Fig. 9E**

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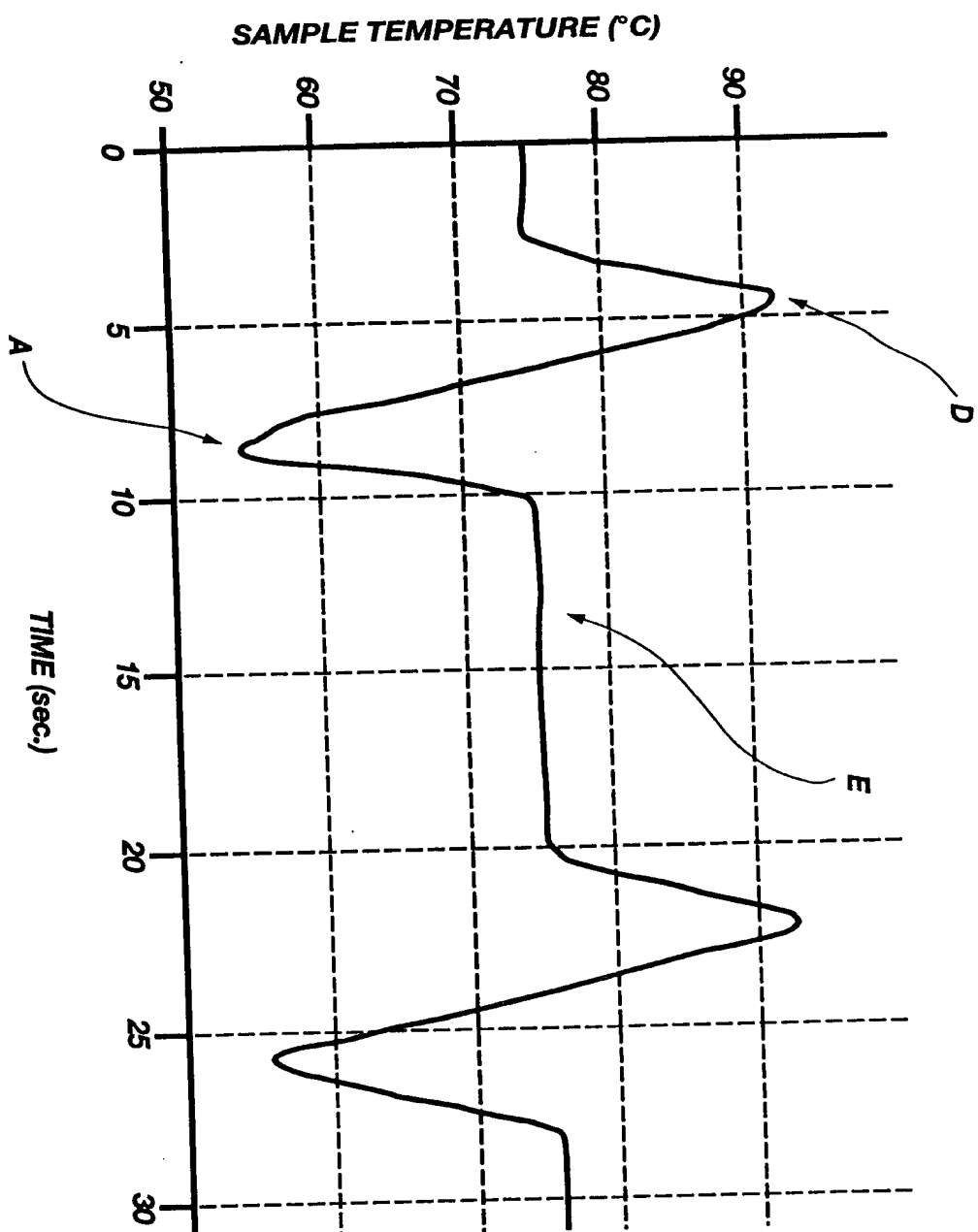
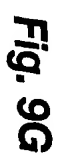


Fig. 9F

[illegible]

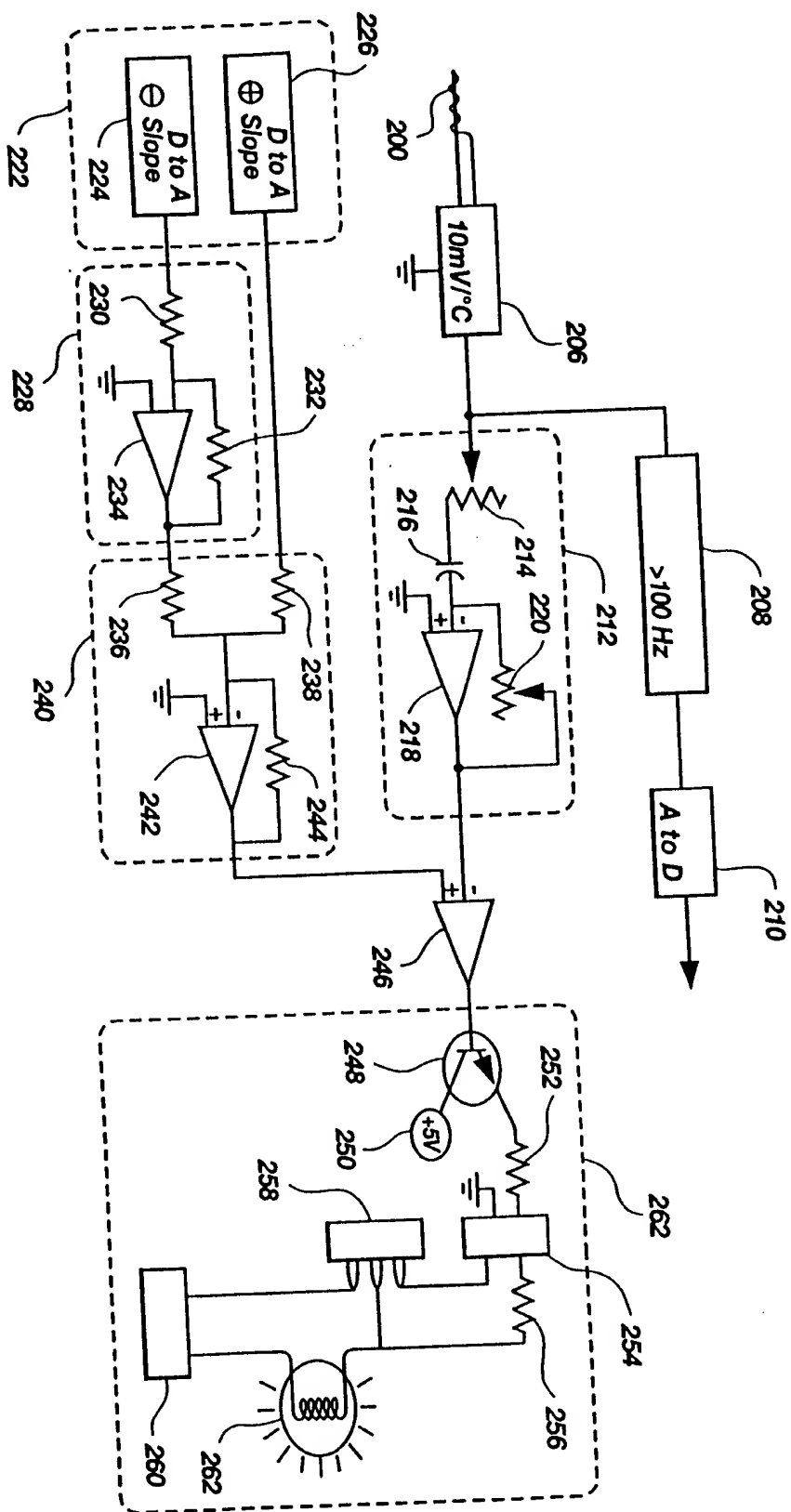


Fig. 10

00009275 0000497

Temperature transition rate ( $^{\circ}\text{C}/\text{sec}$ )

1

2

5

10

20



← 536 bp product

Fig. 10A

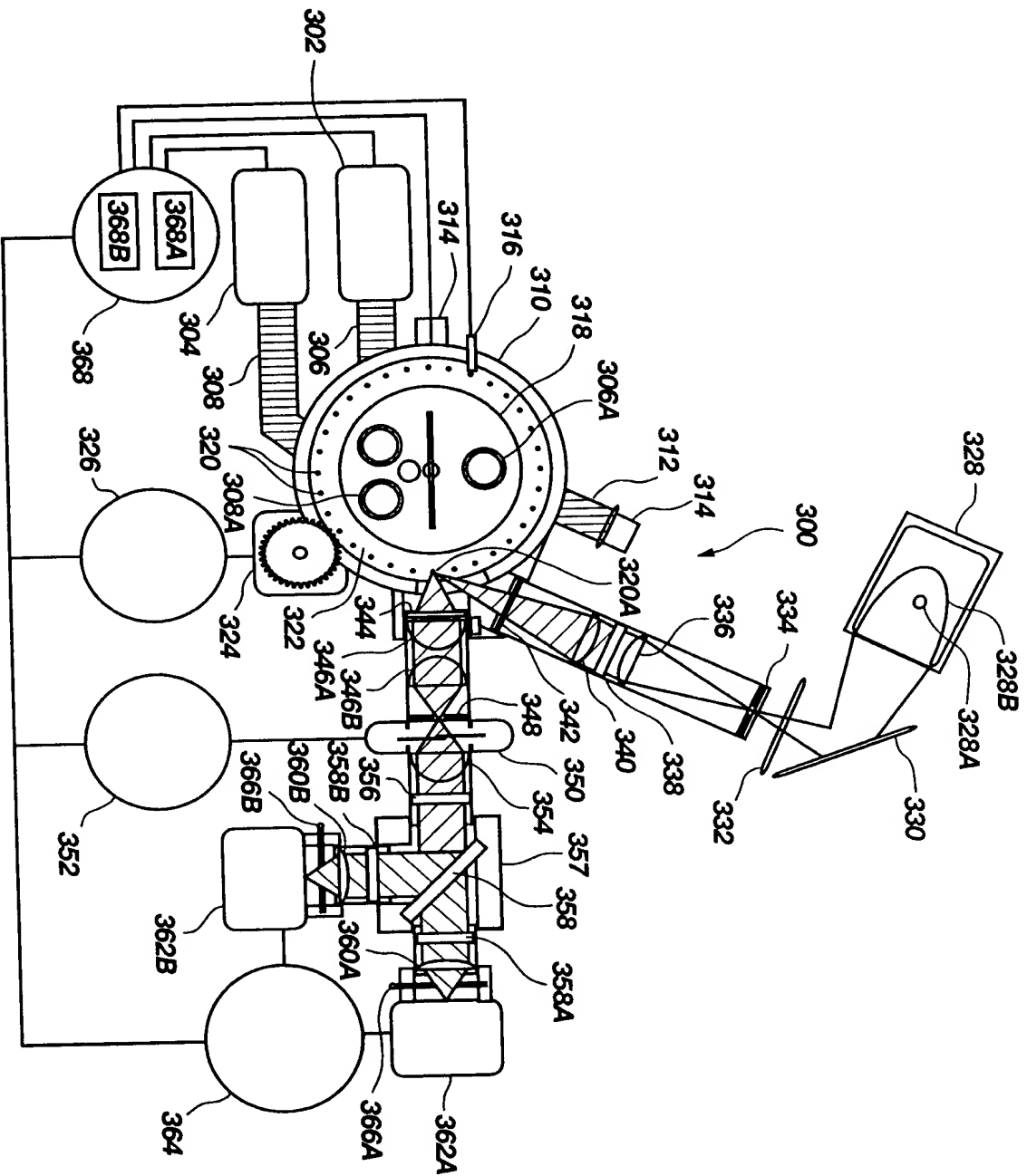
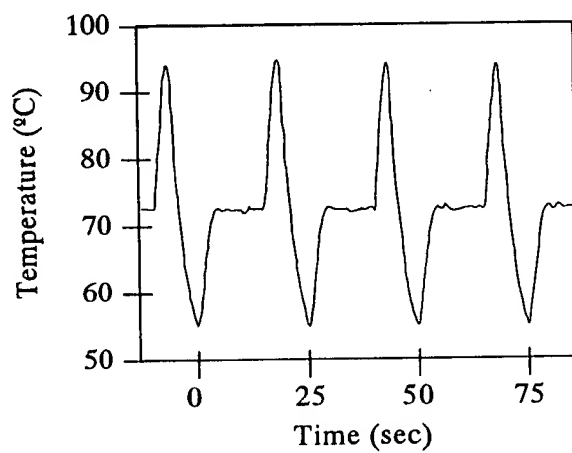
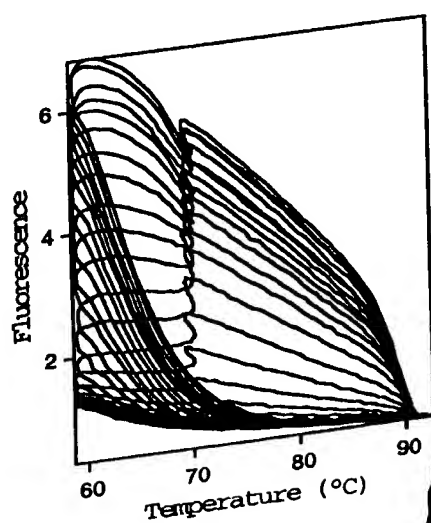


Fig. 11

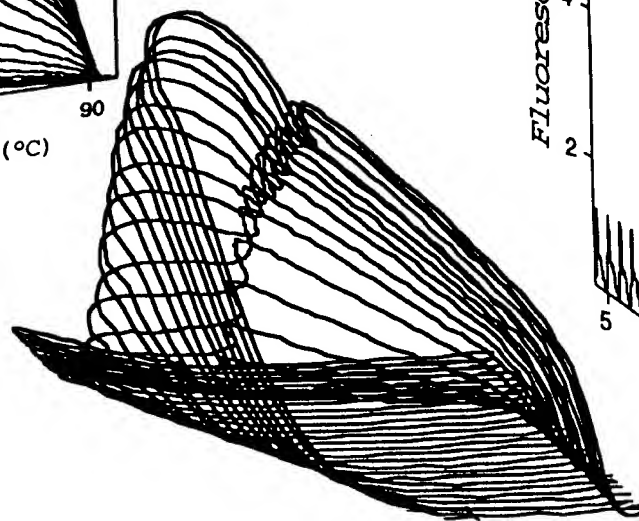


**Fig. 11A**

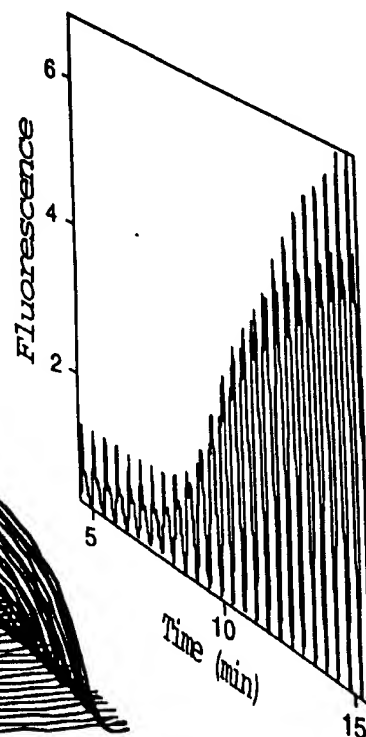




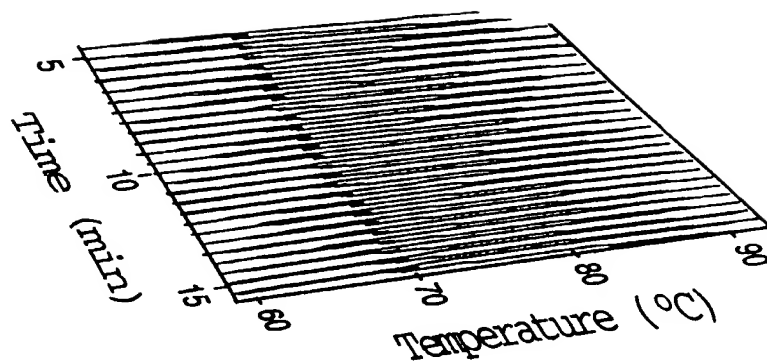
**Fig. 12C**



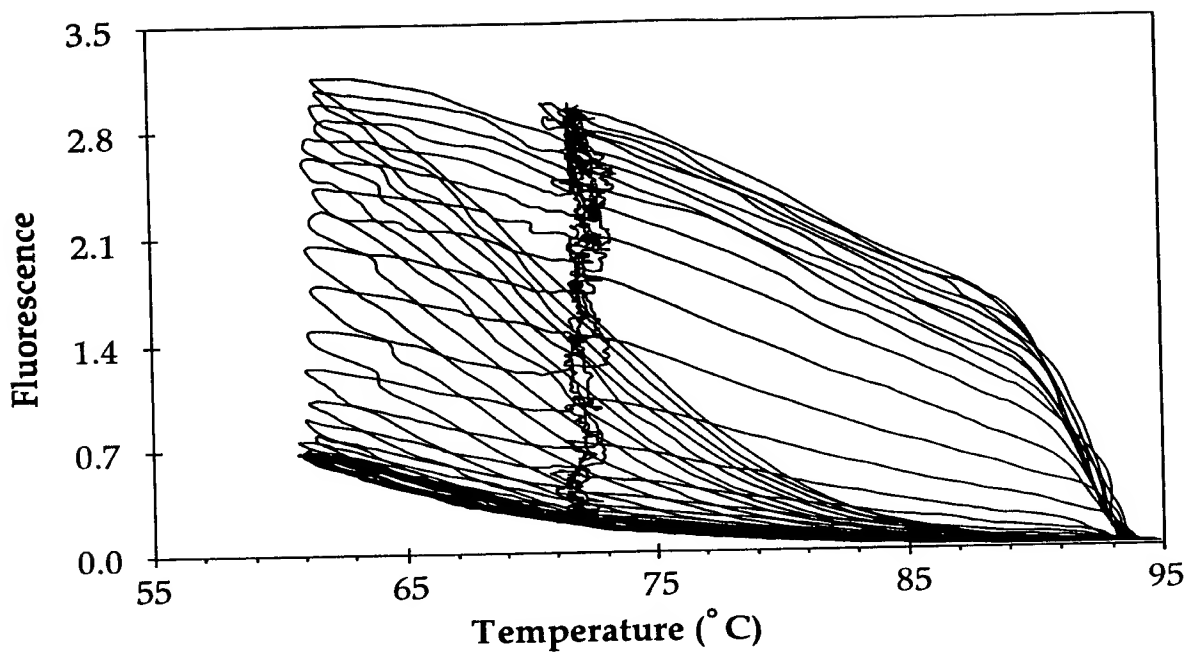
**Fig. 12**



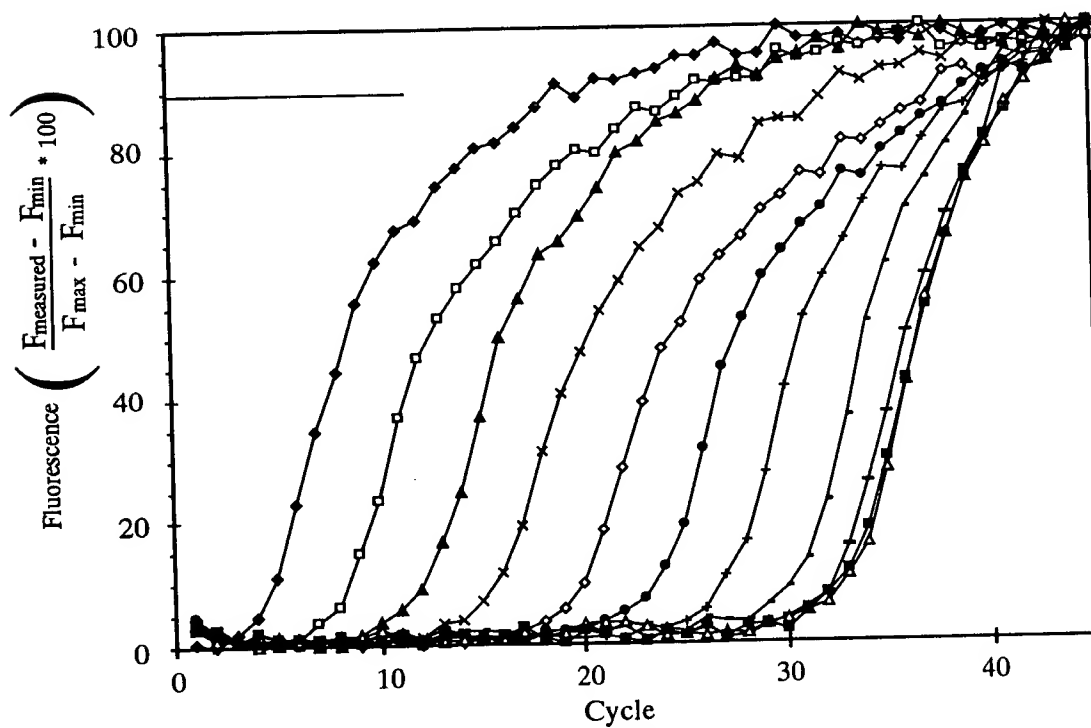
**Fig. 12B**



**Fig. 12A**



**Fig. 13**

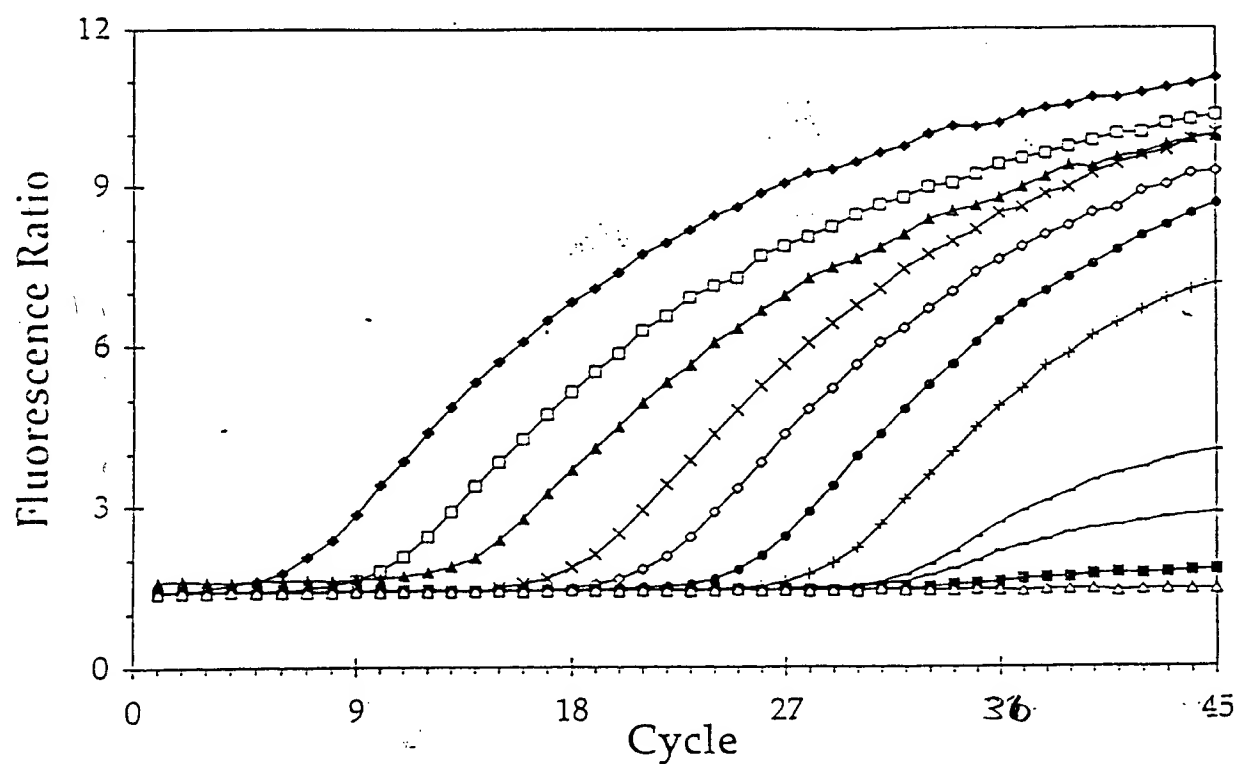


**Fig. 14**

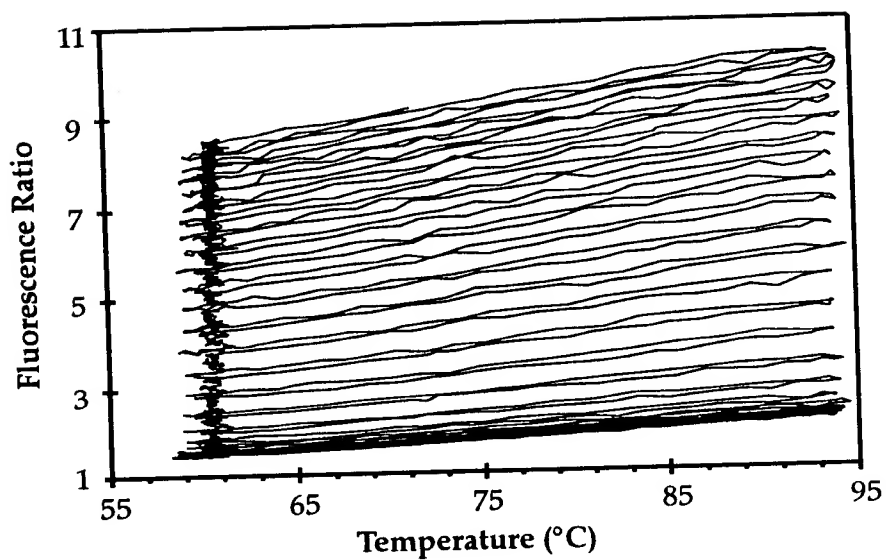
Initial  
Template Copies

- ◆  $10^9$
- $10^8$
- ▲  $10^7$
- ×  $10^6$
- ◇  $10^5$
- $10^4$
- +  $10^3$
- $10^2$
- 10
- 1
- △ 0

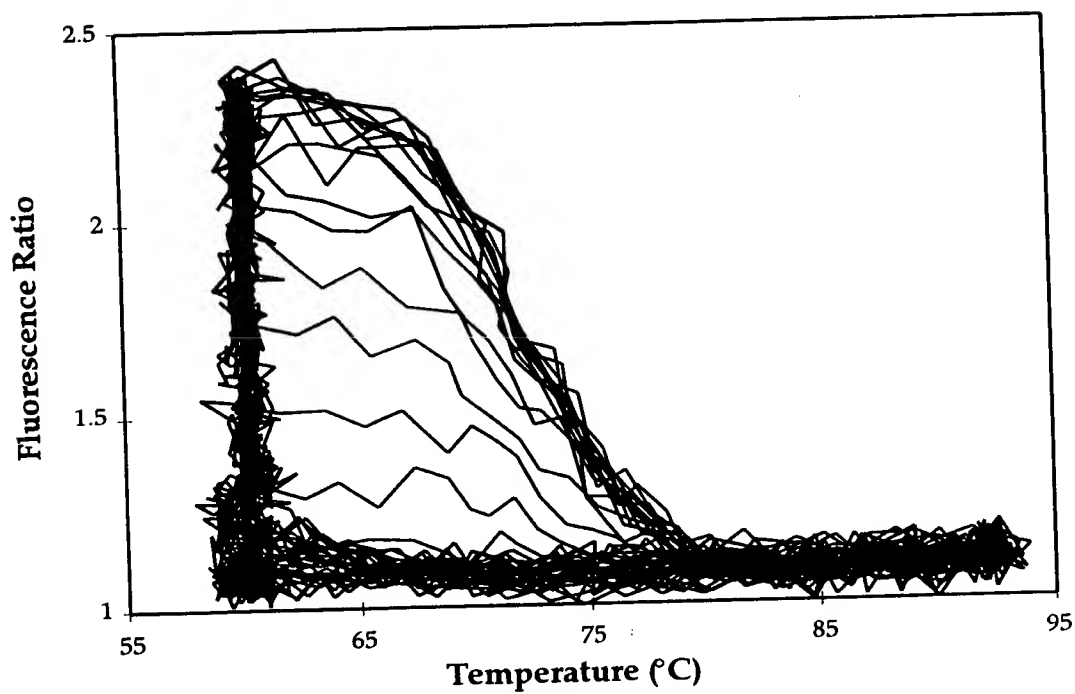
**Fig. 14A**



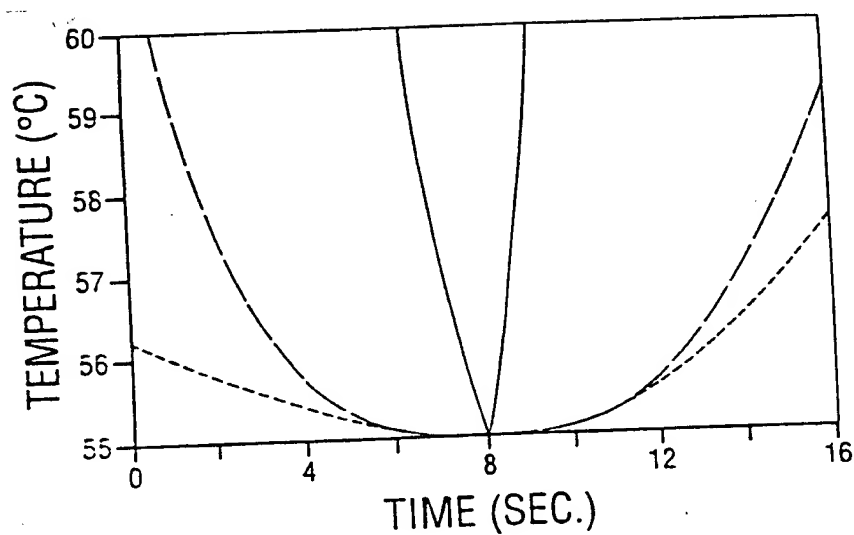
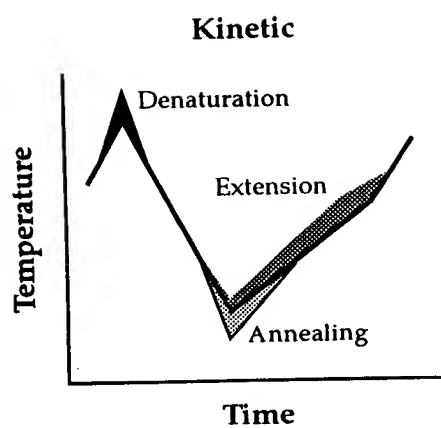
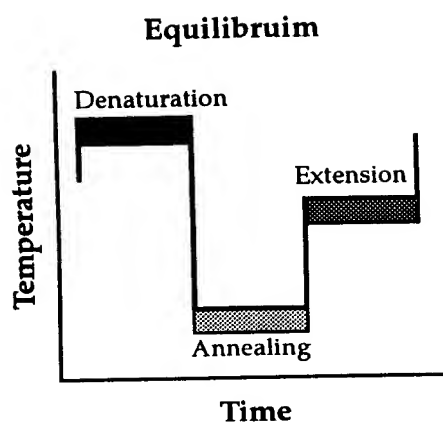
**Fig. 15**

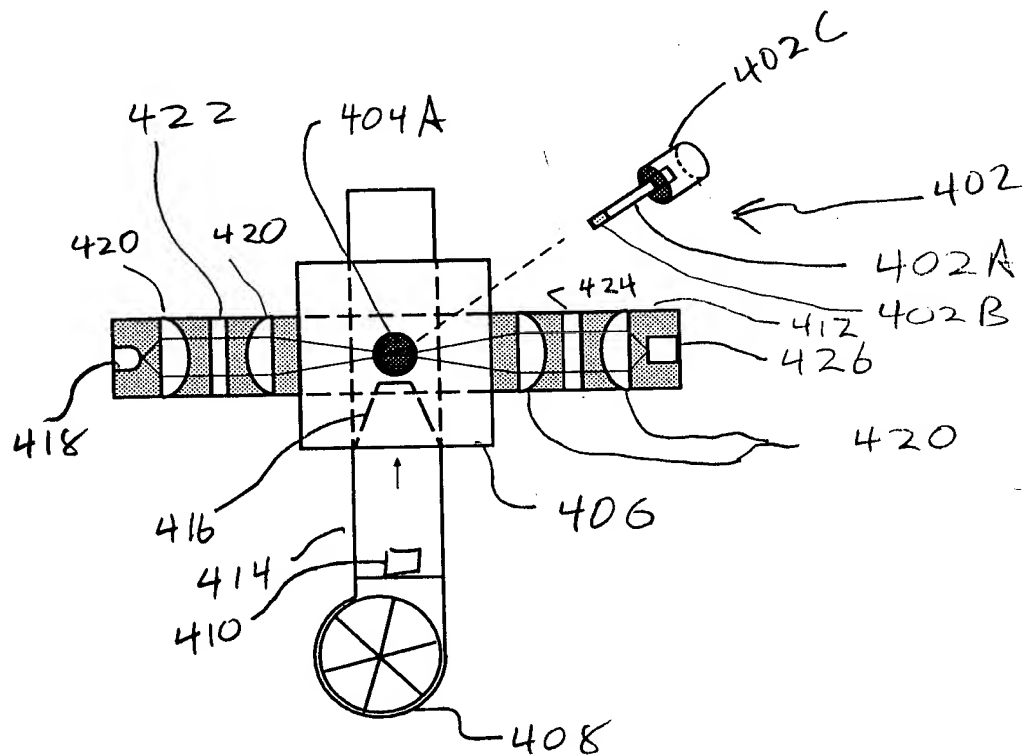


**Fig. 16**



**Fig. 17**





**Fig. 19**

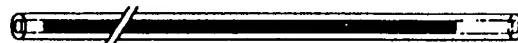
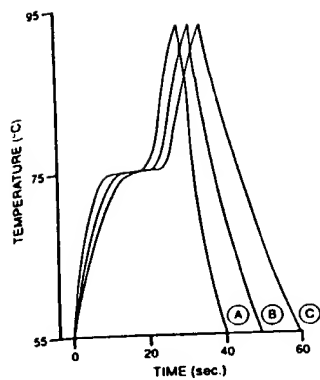


Fig. 19A



Fig. 19B

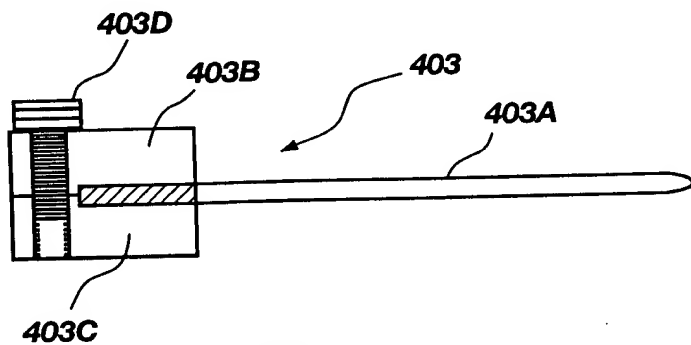


Fig. 19C

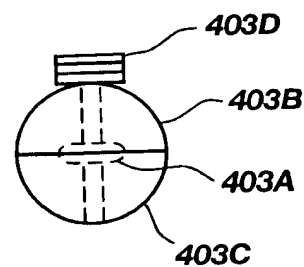


Fig. 19D

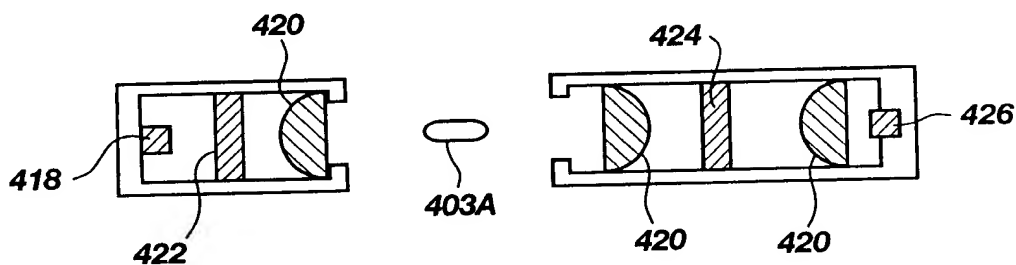
**Fig. 19E**



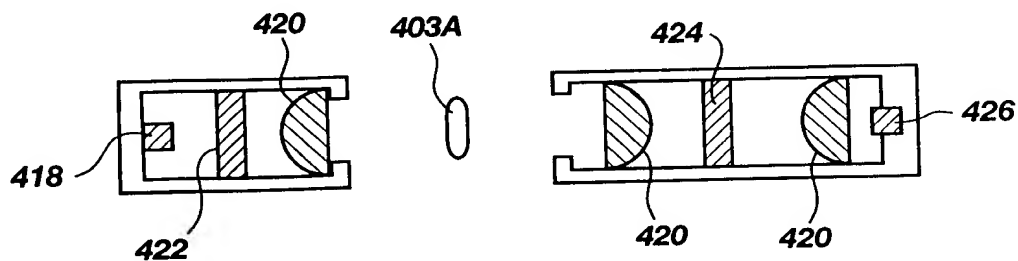
**Fig. 19F**



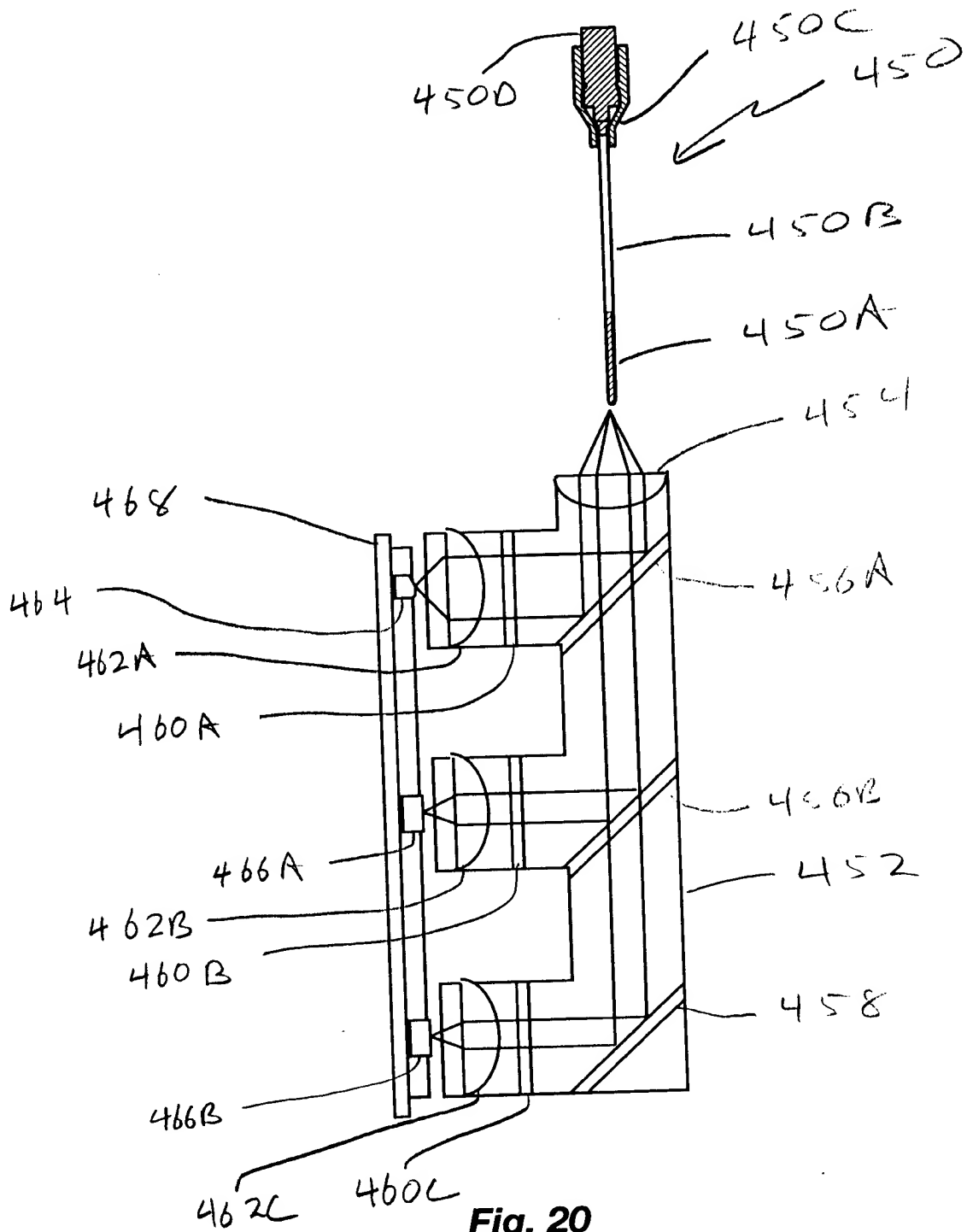
**Fig. 19G**



**Fig. 19H**

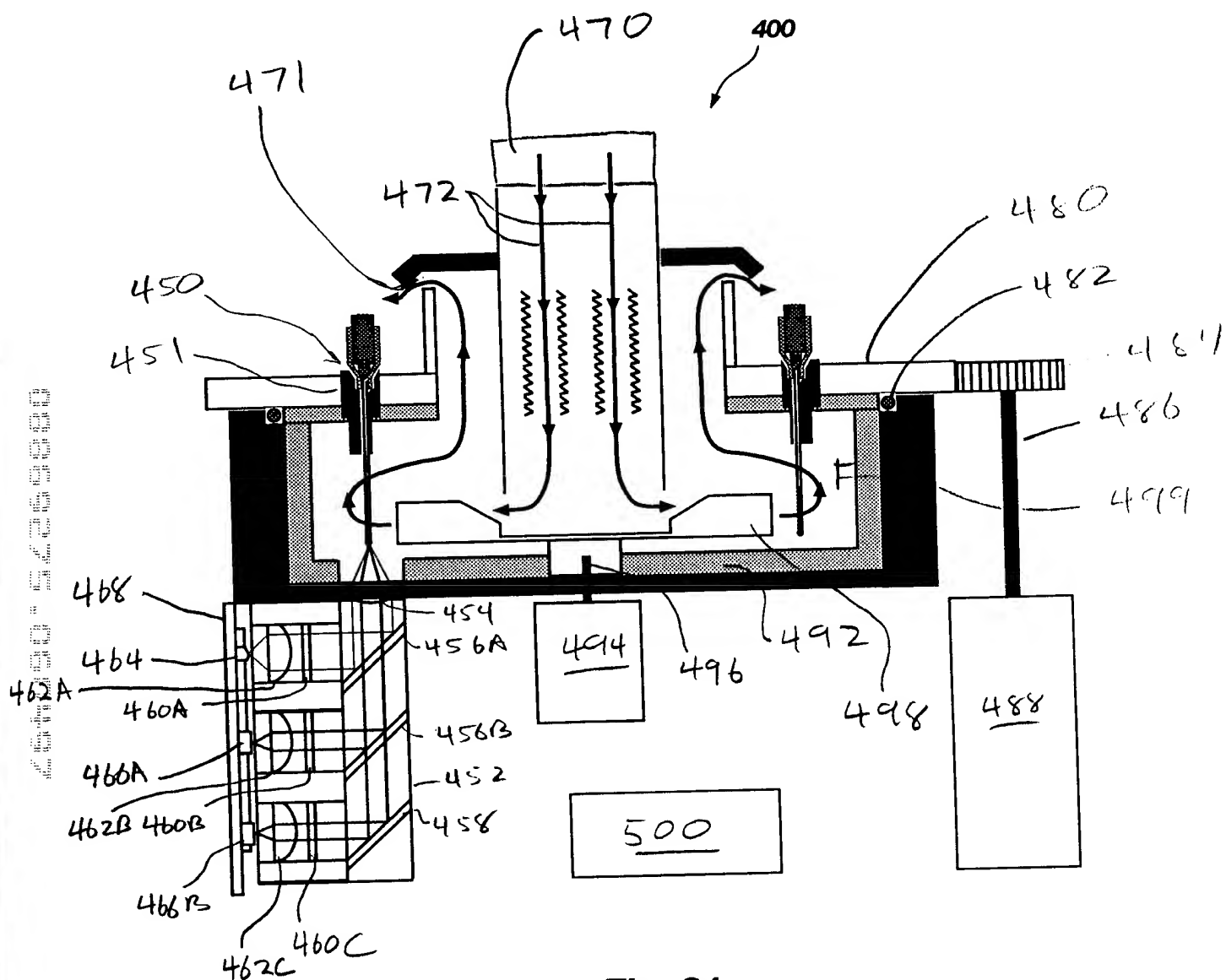


**Fig. 19I**



**Fig. 20**





**Fig. 21**



Load  
Sample

**Fig. 21A**



Centrifuge

**Fig. 21B**



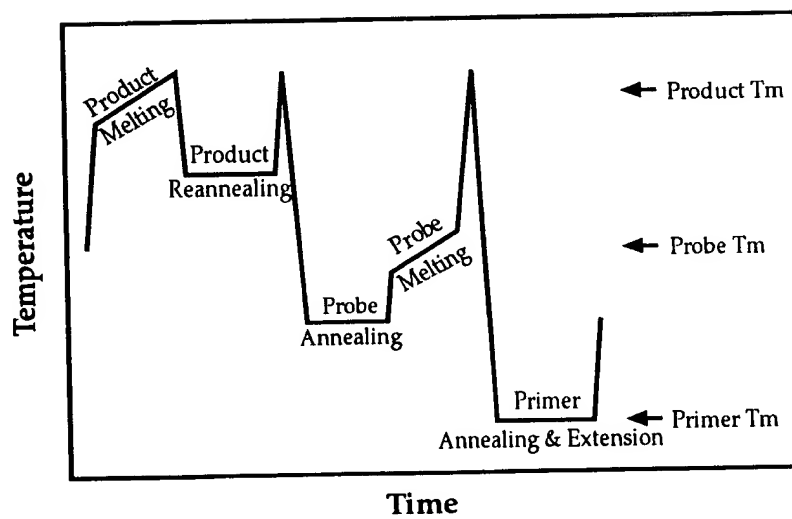
Seal

**Fig. 21C**

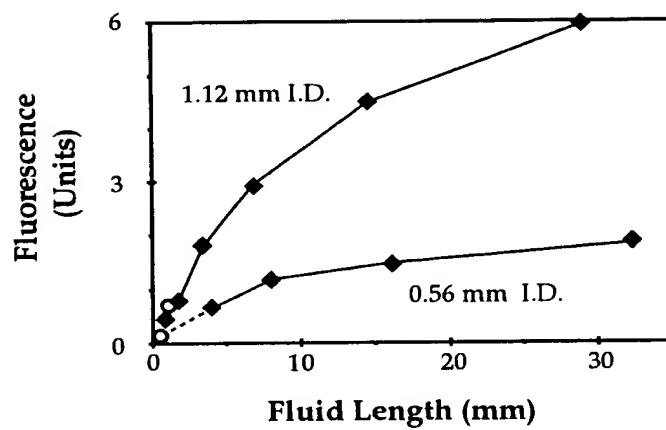


Cycle

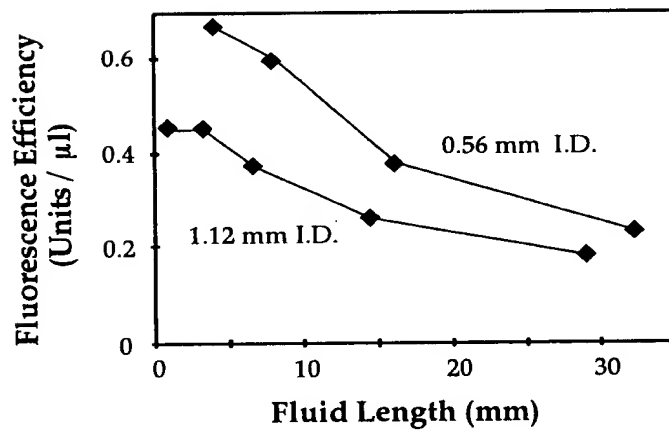
**Fig. 21D**



**Fig. 22**



**Fig. 22A**



**Fig. 22B**

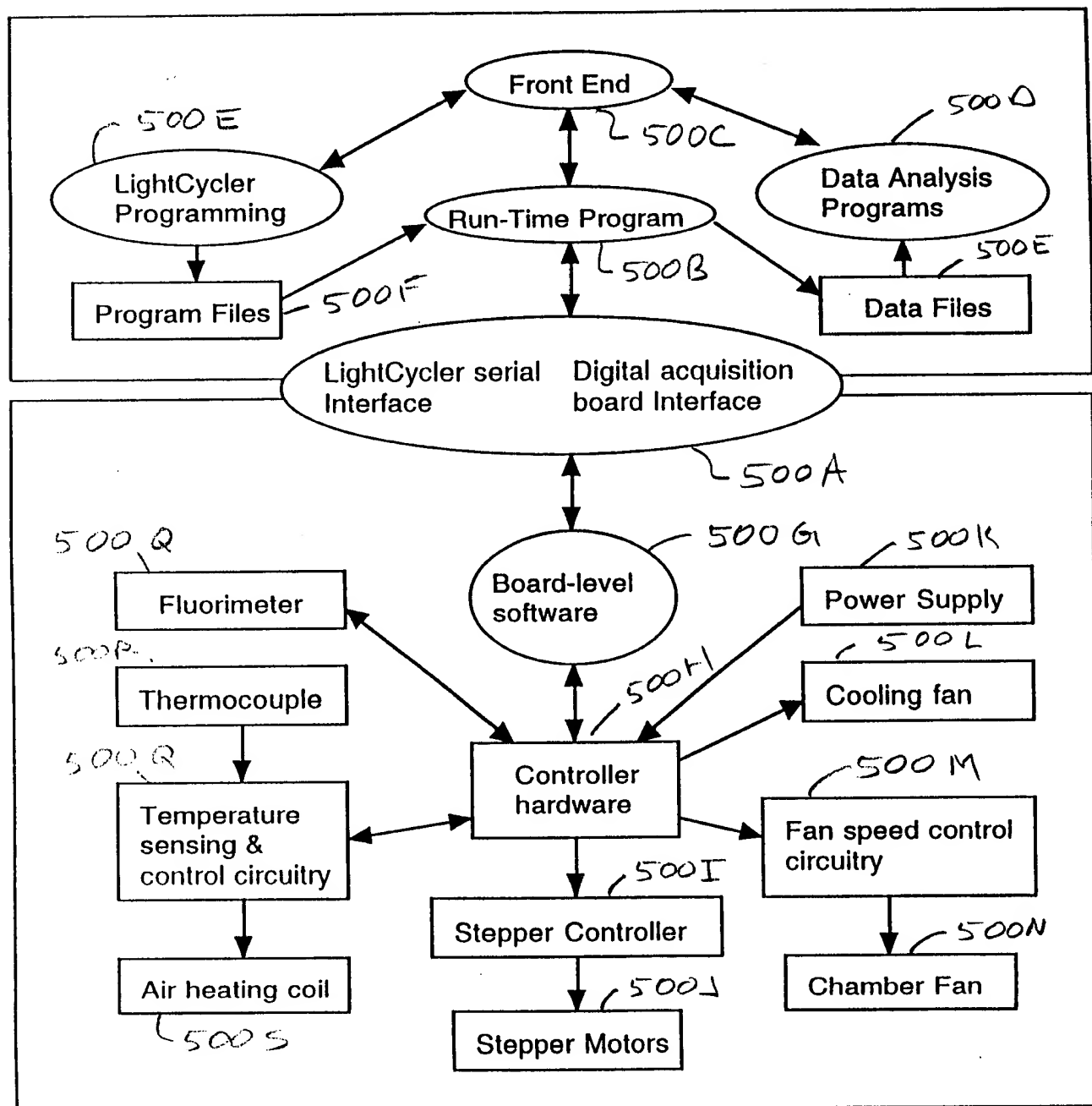


Fig. 22C

Fig. 22D

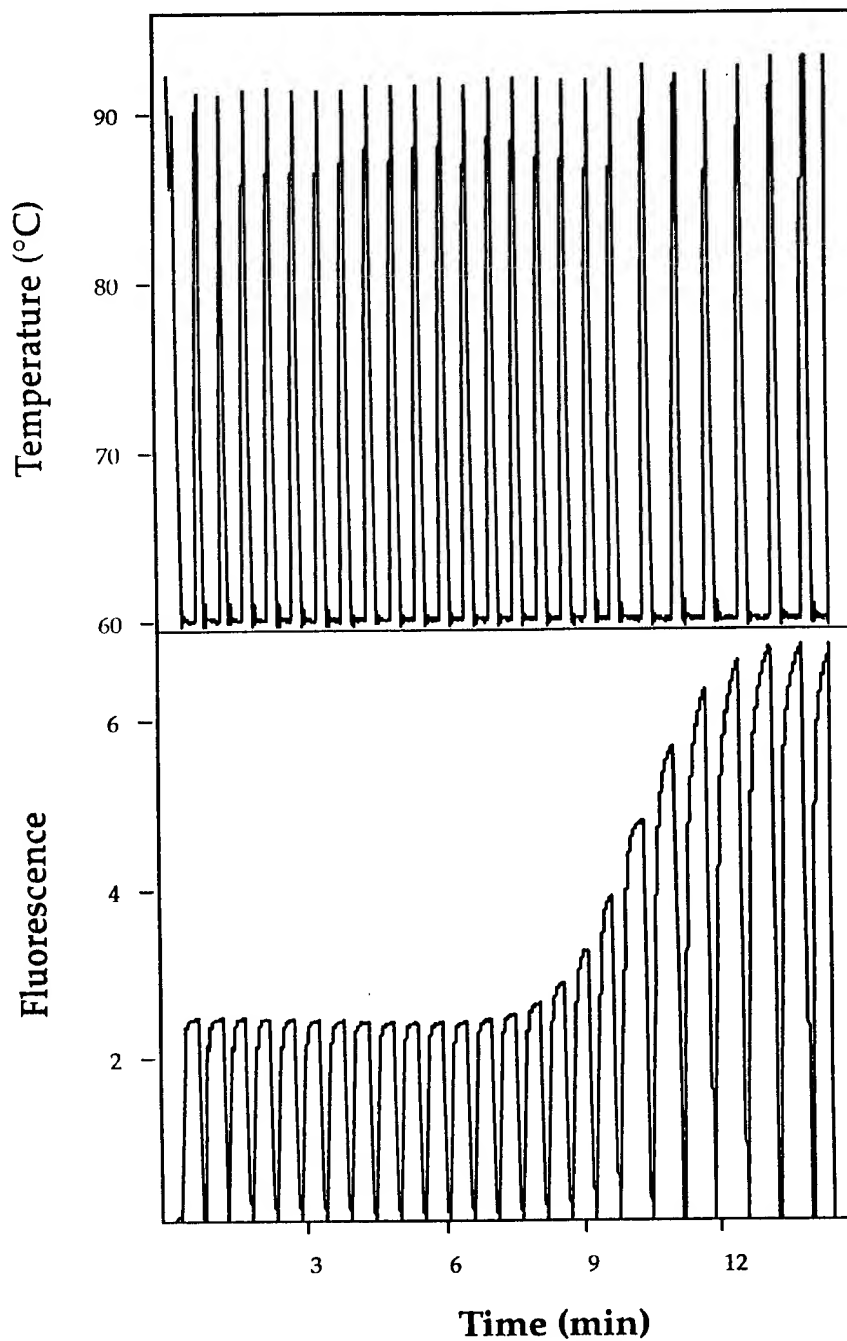
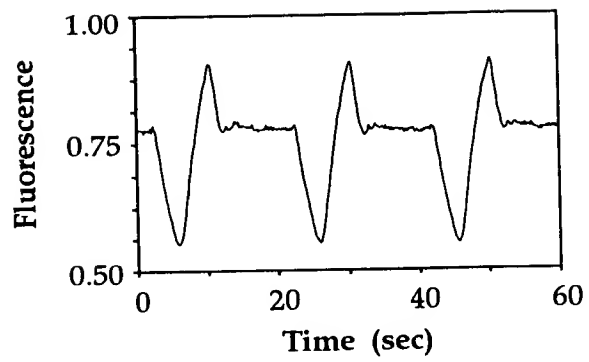
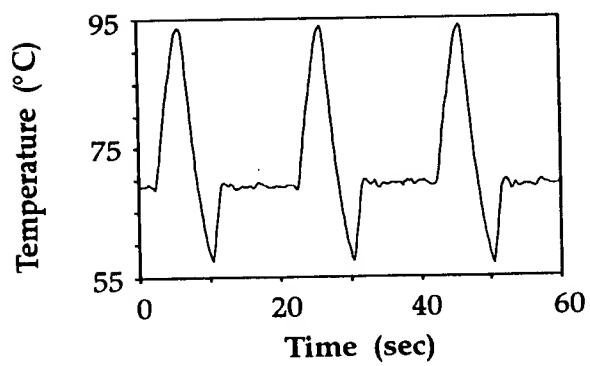


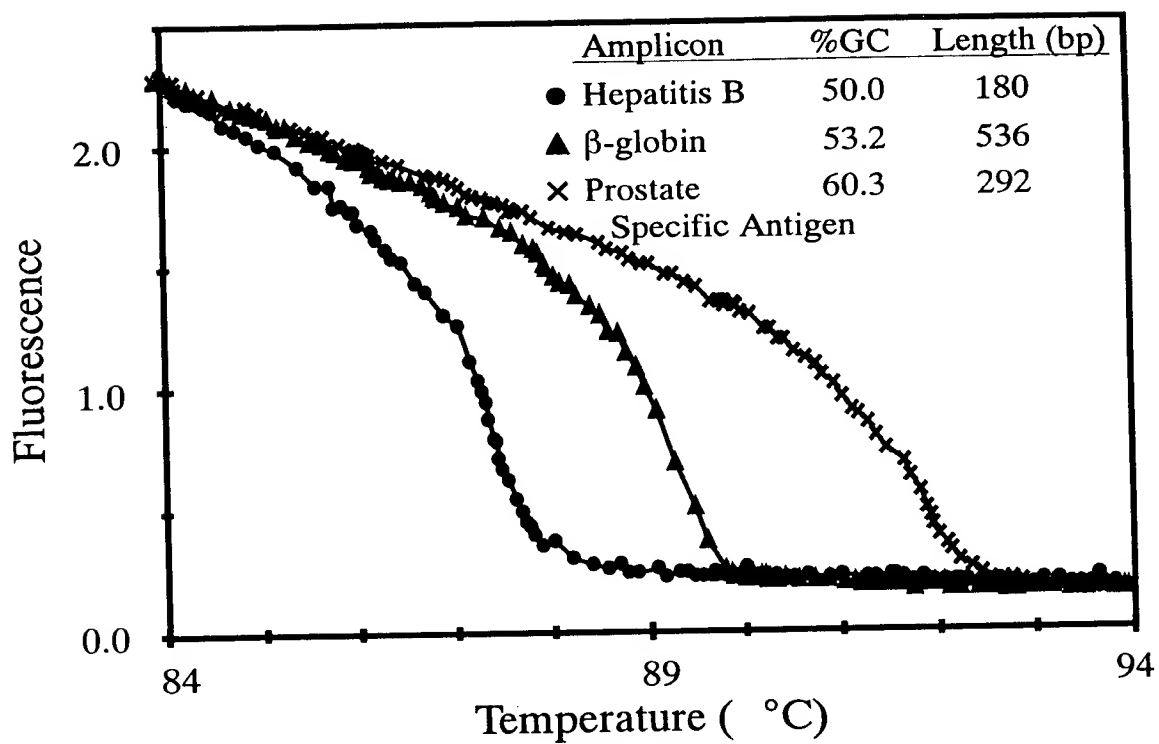
Fig 22E



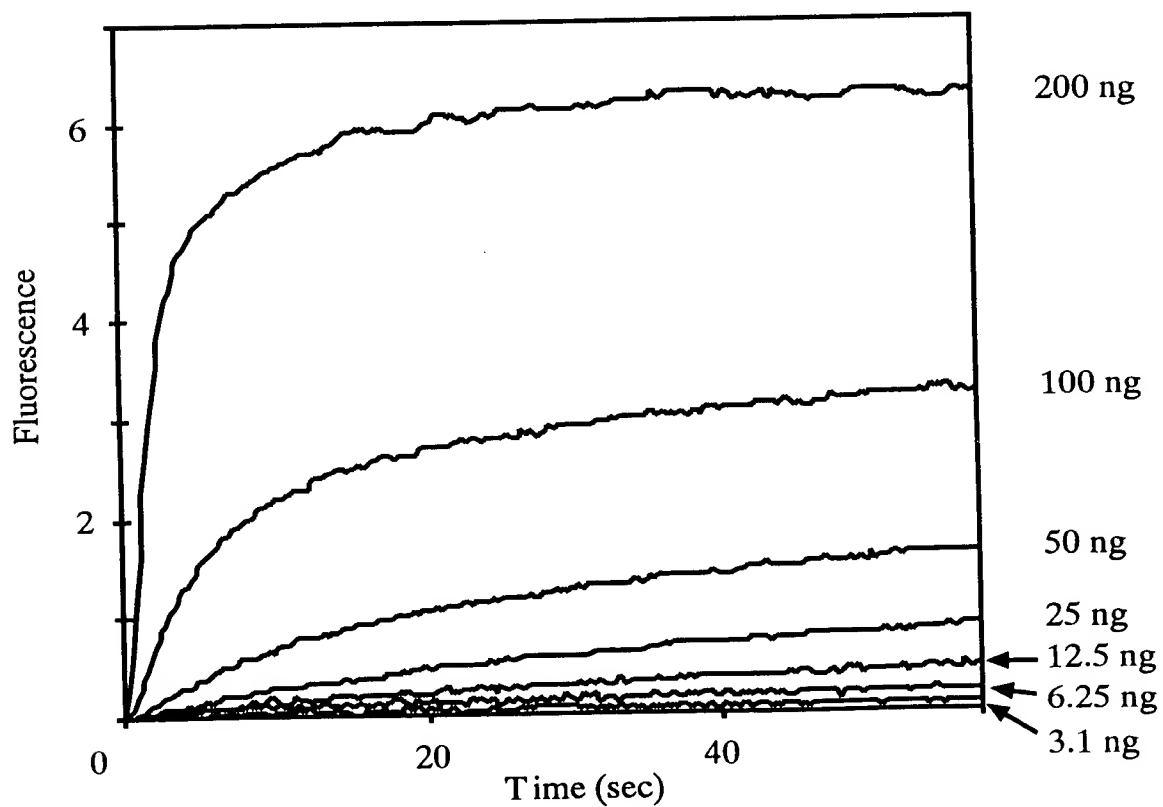
**Fig. 23**



**Fig. 24**



**Fig. 25**



**Fig. 26**

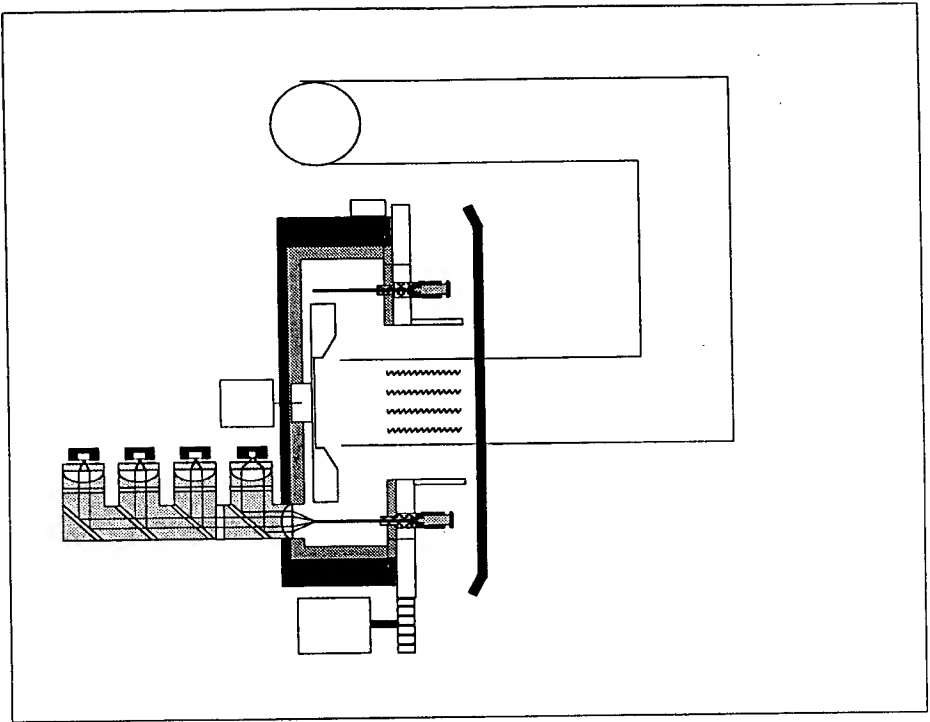


Fig. 27A

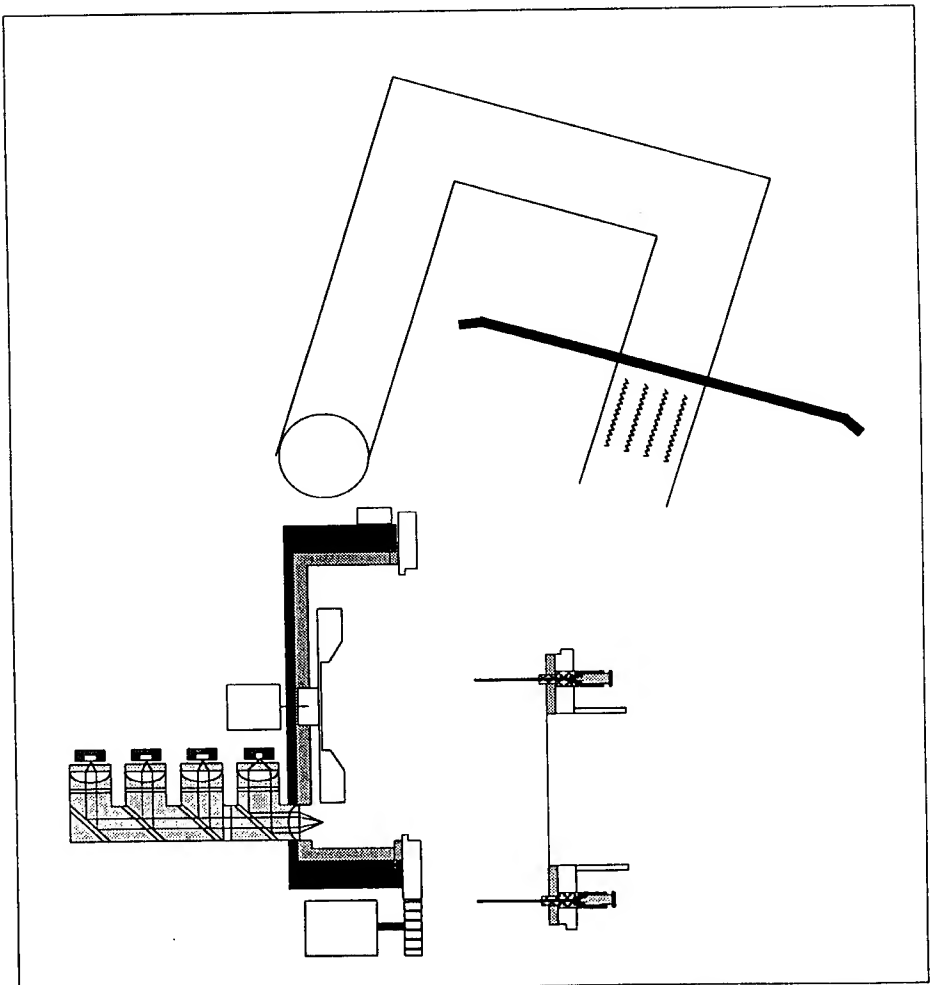
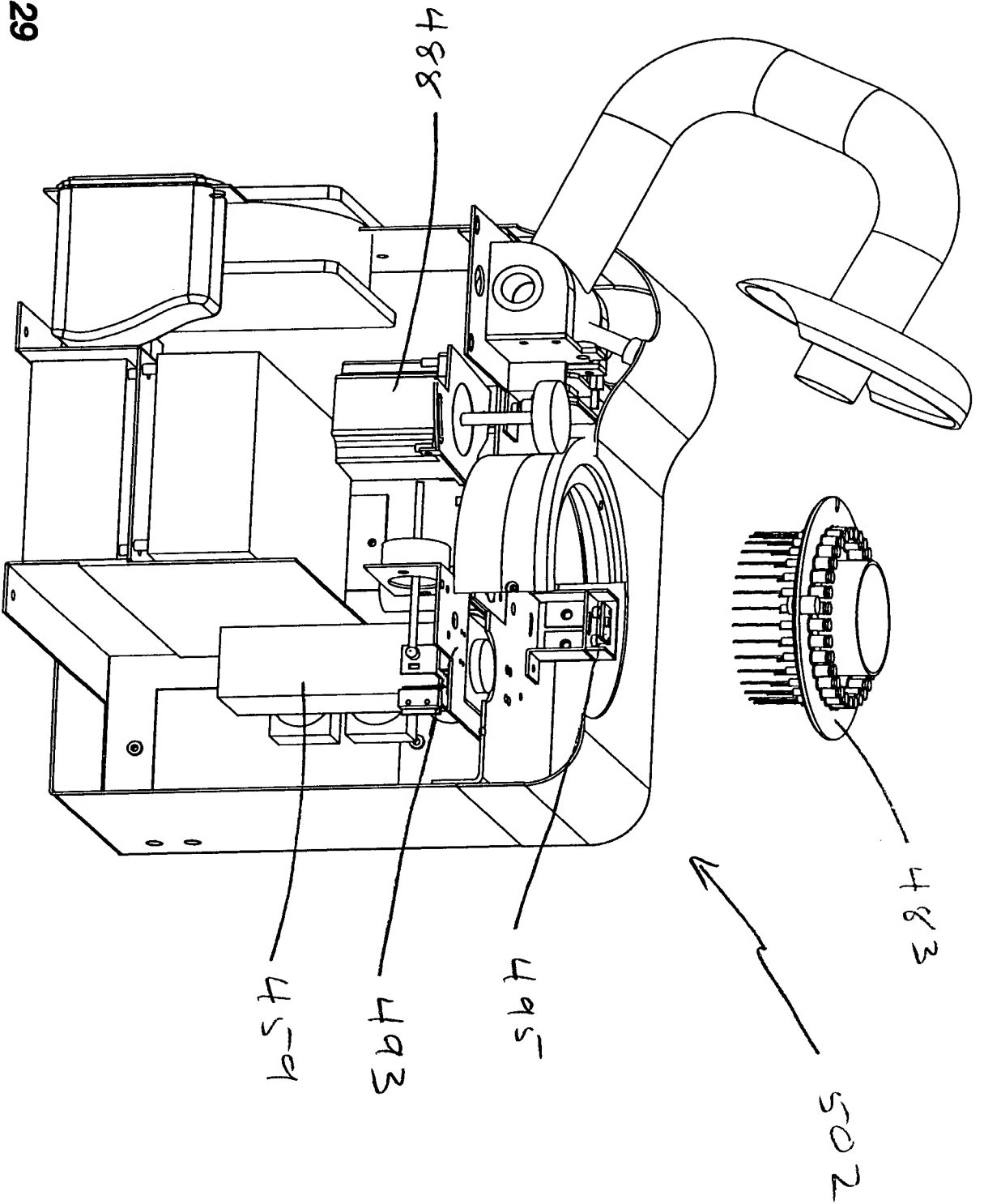


Fig. 27B

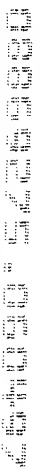




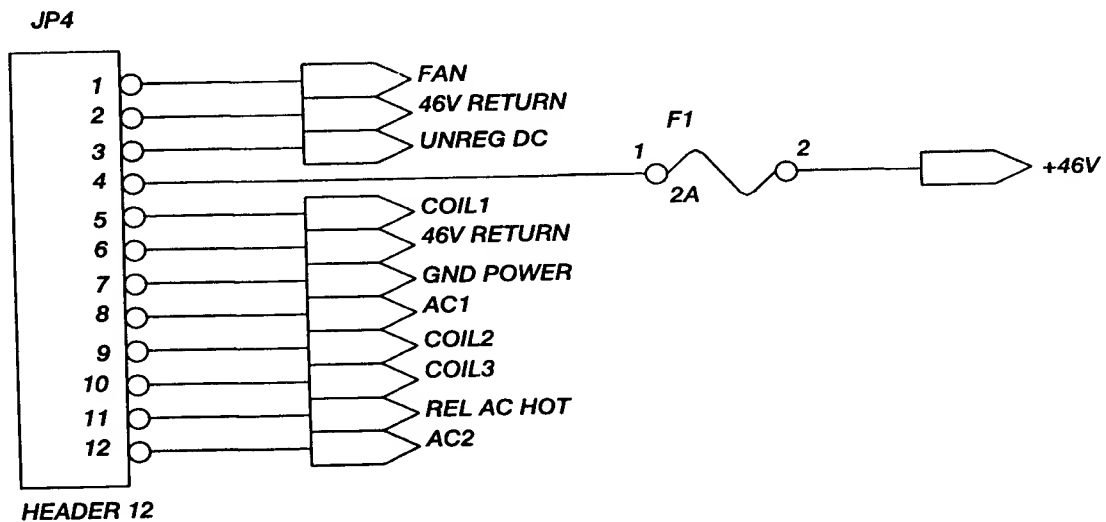
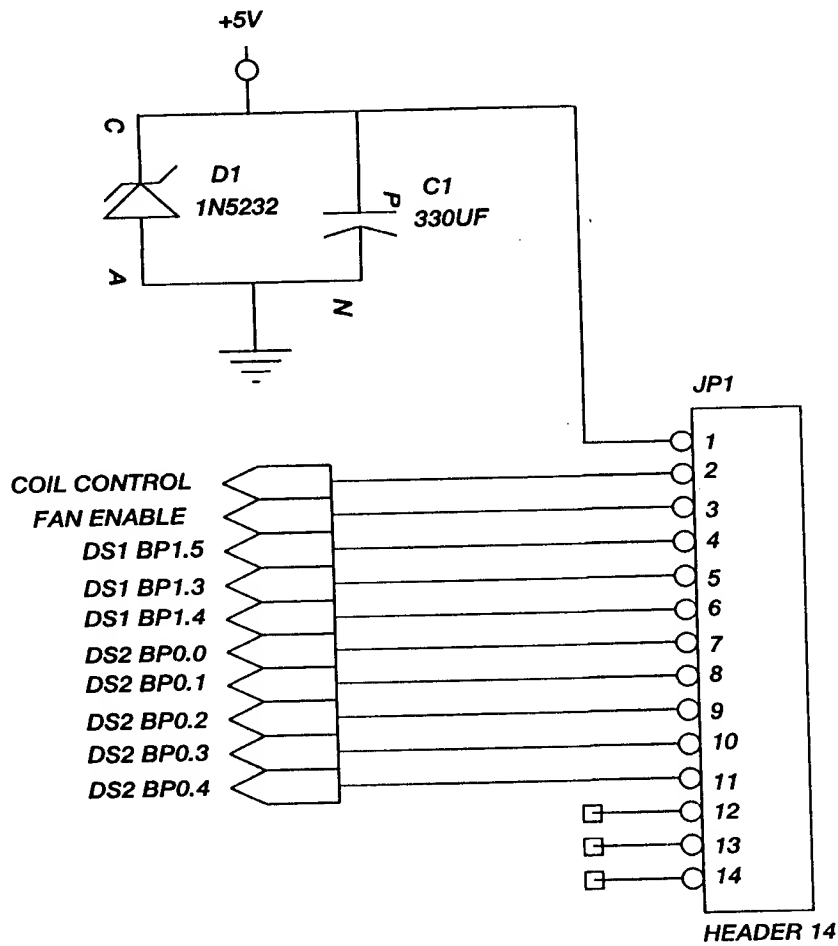
Fig. 29







**Fig. 30B**



**Fig. 30C**

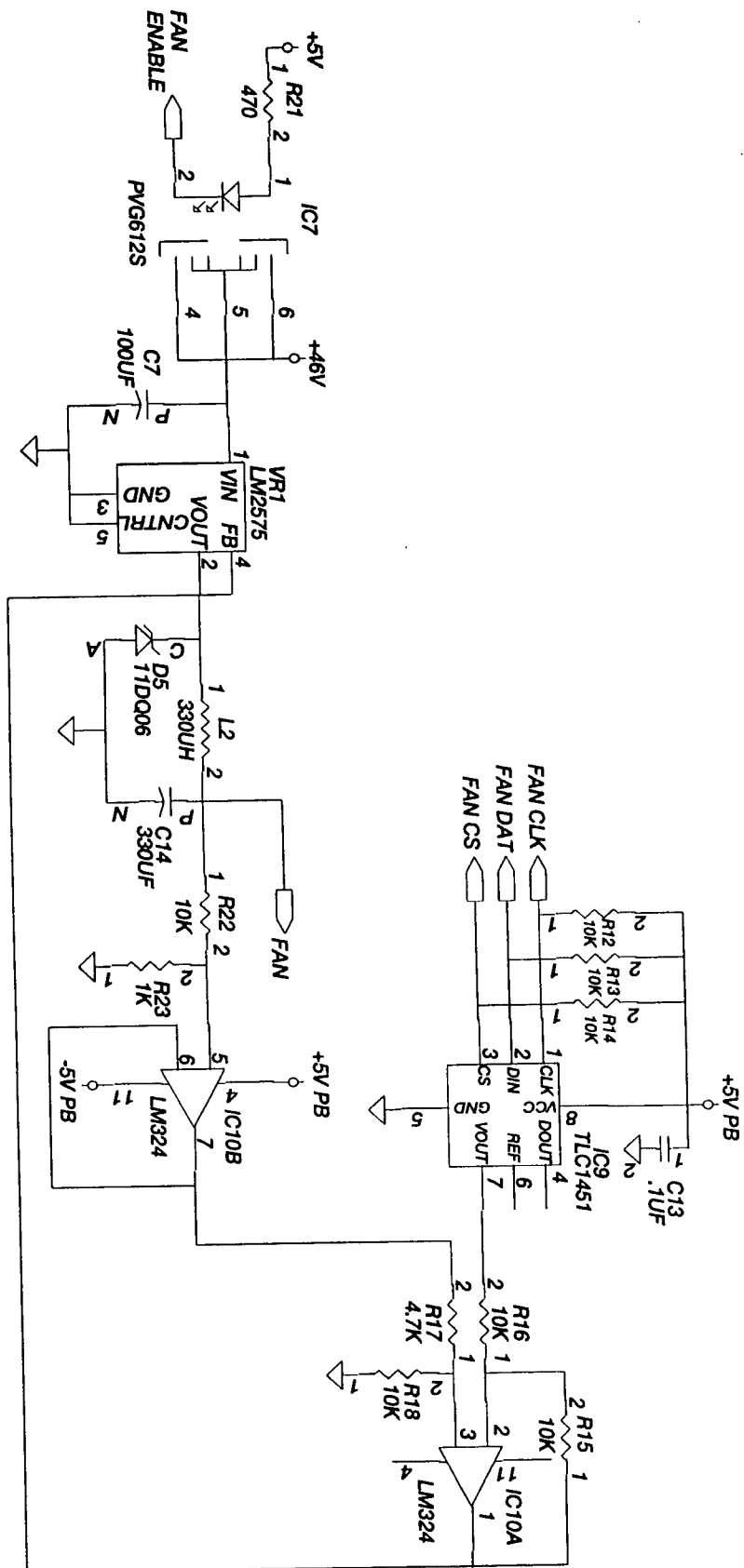
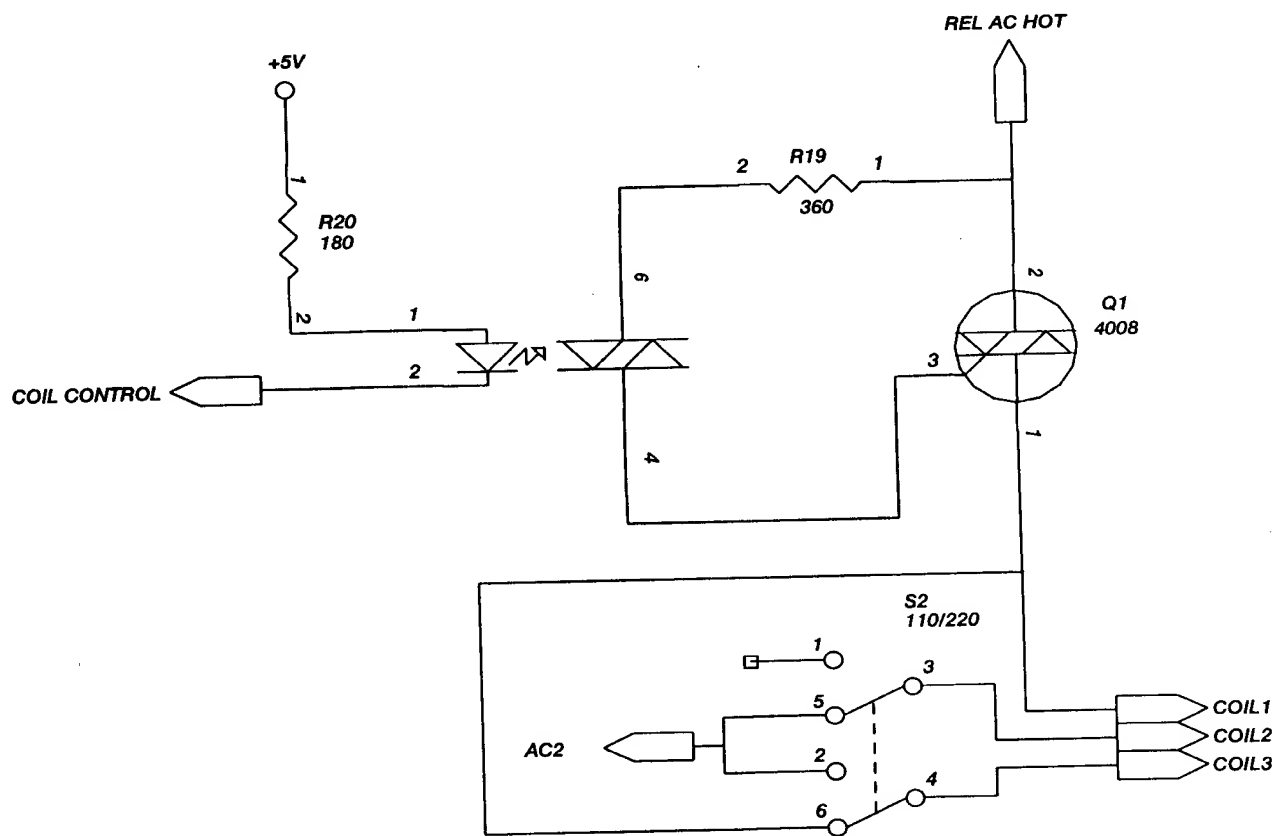
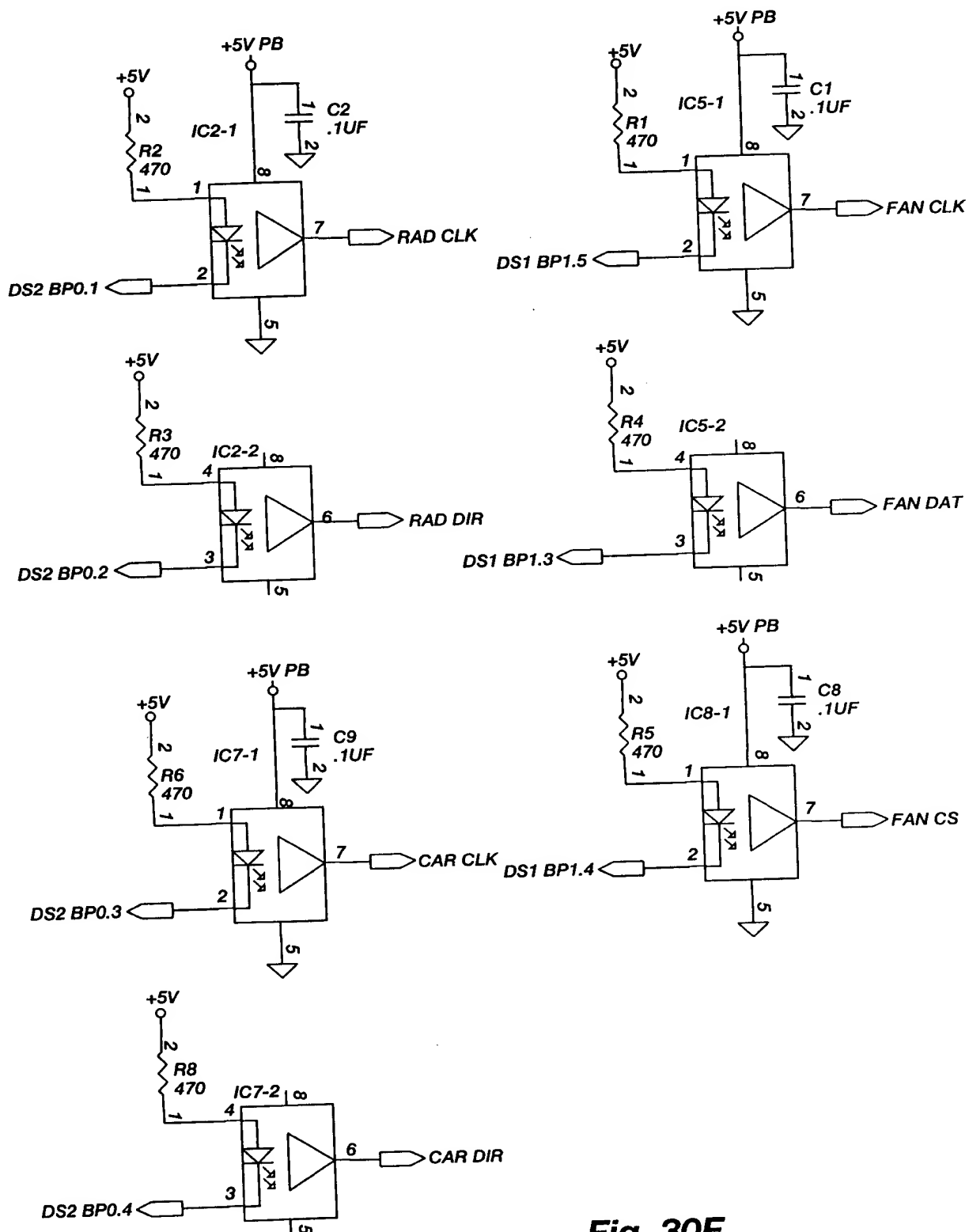


Fig. 30D



**Fig. 30E**



**Fig. 30F**



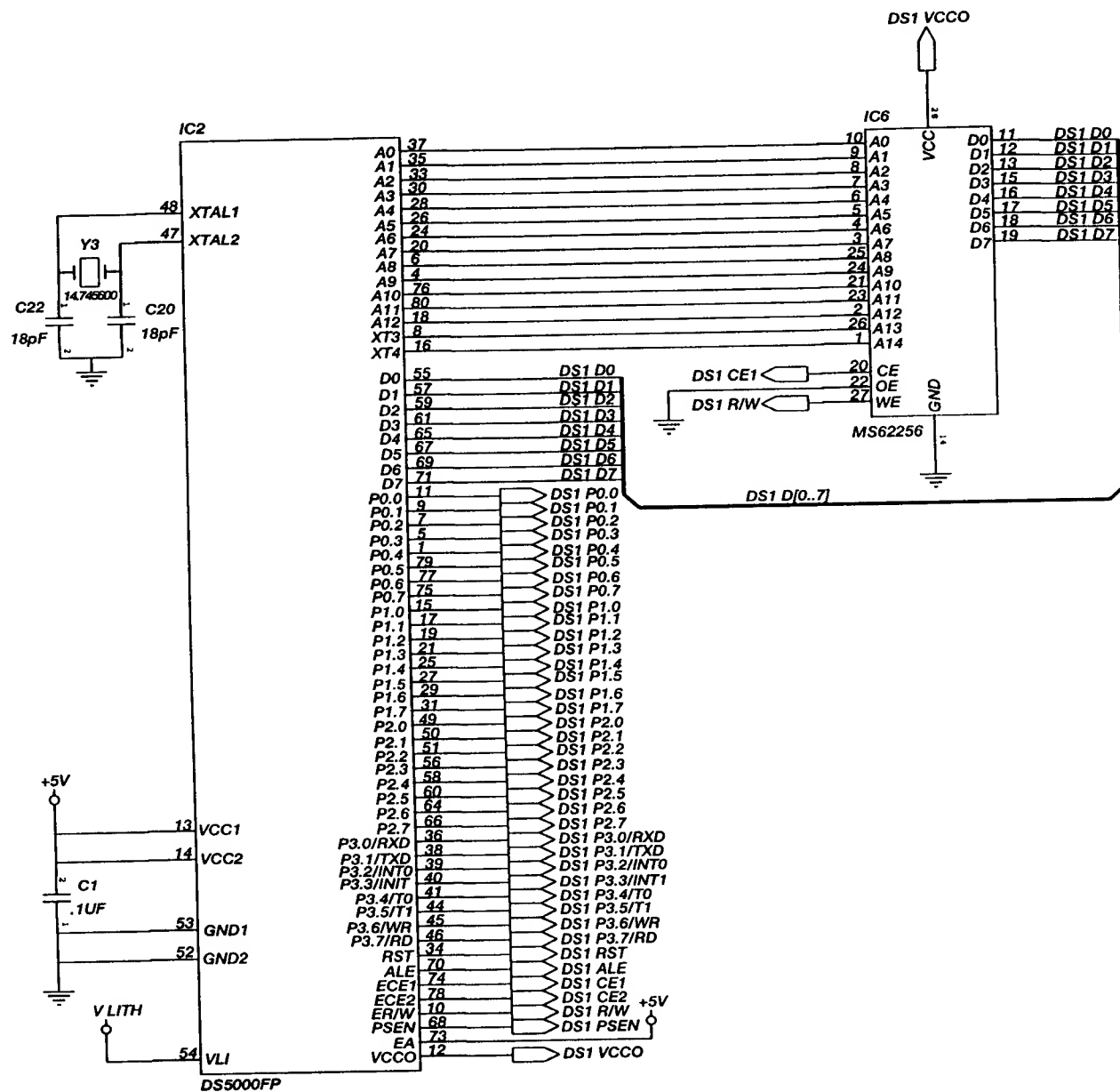
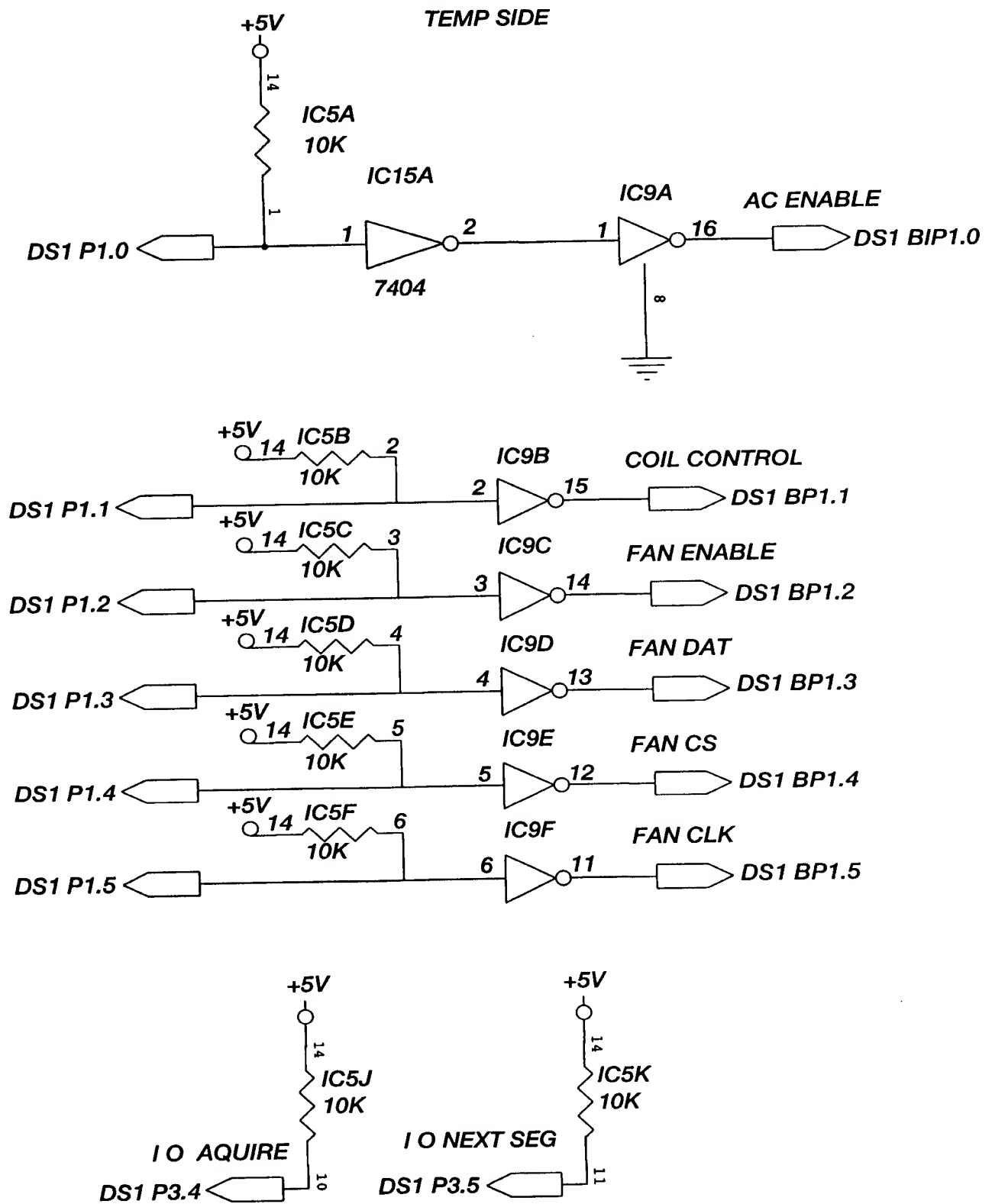


Fig. 30G



**Fig. 30H**

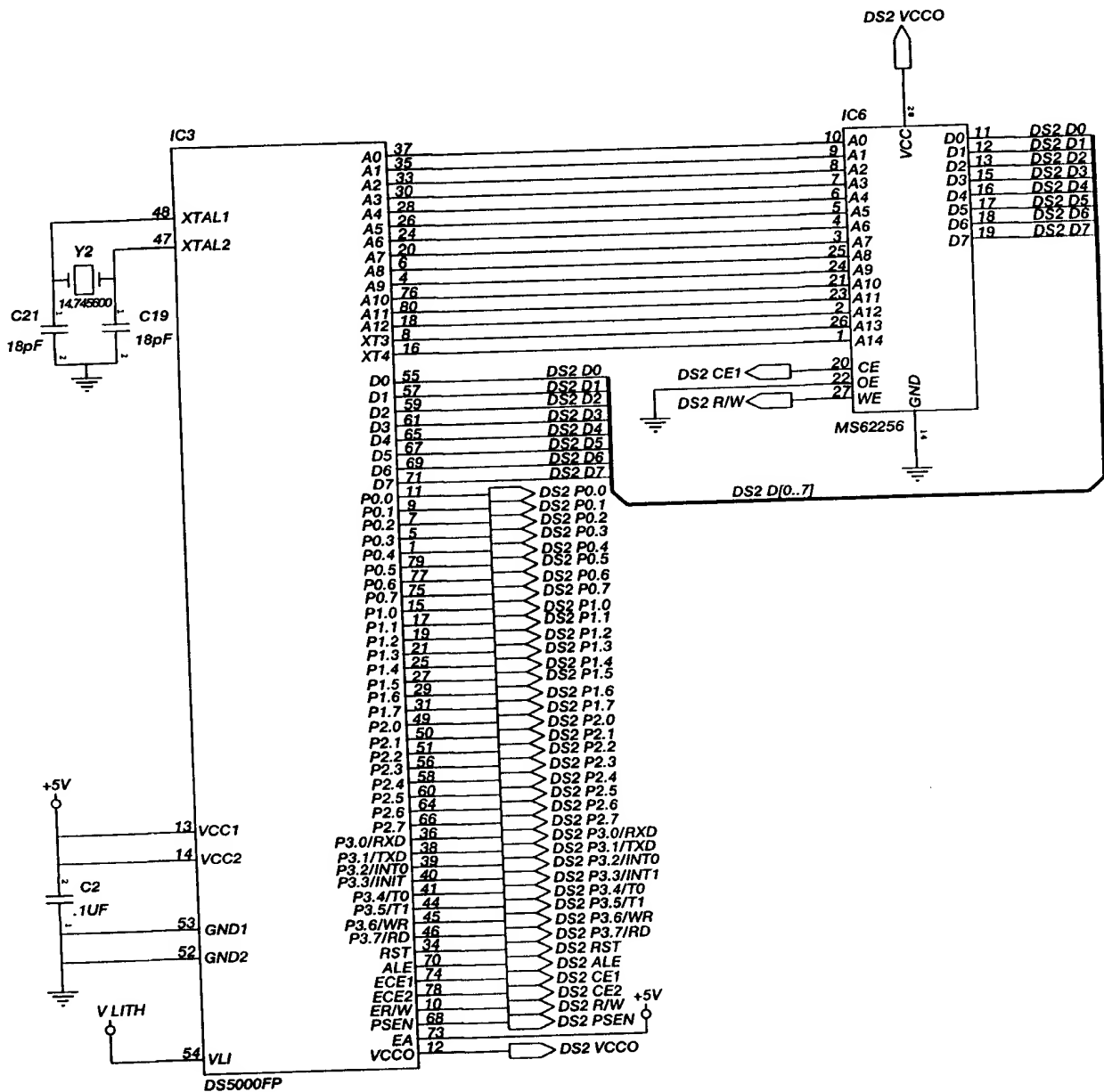


Fig. 30I

# STEP SIDE

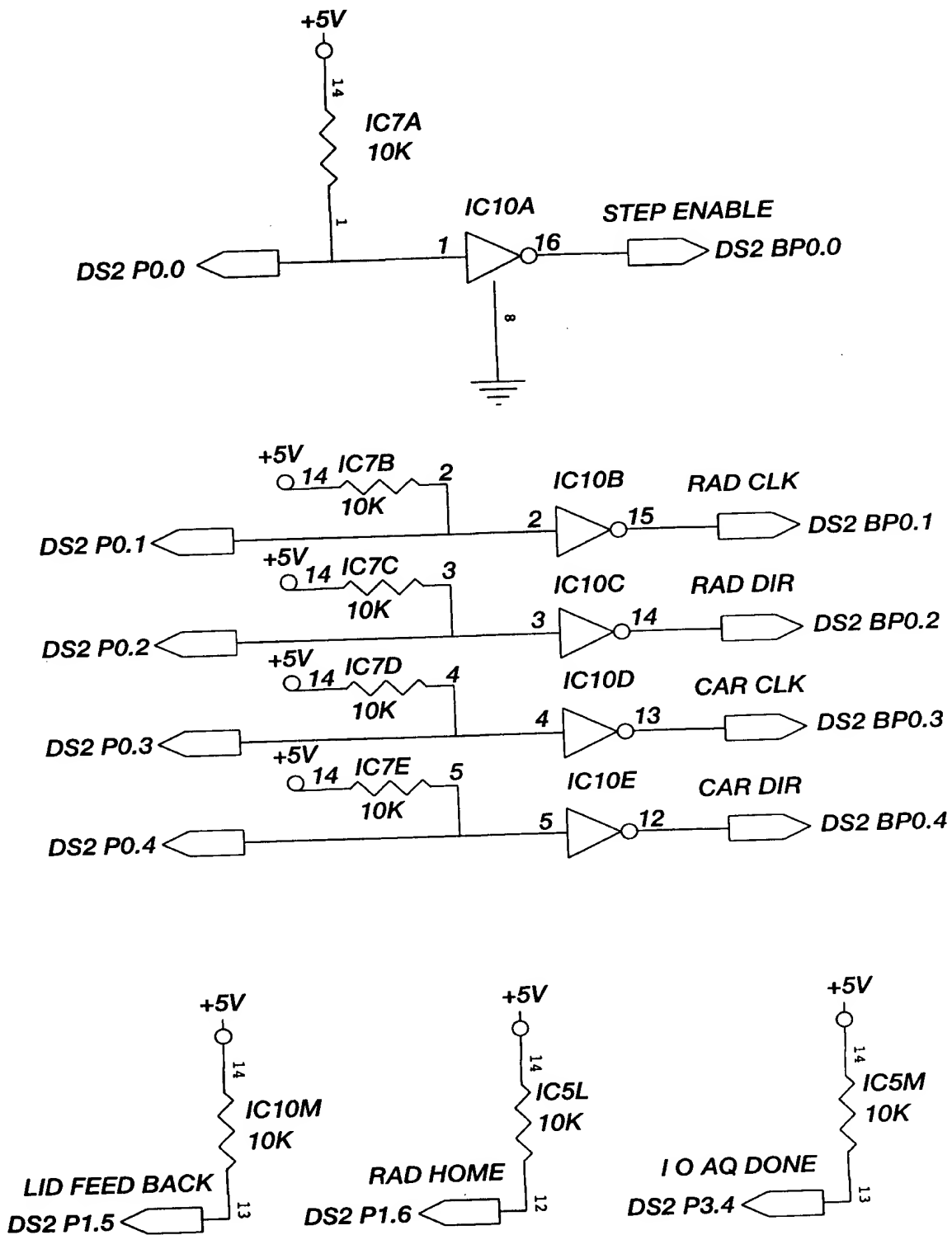
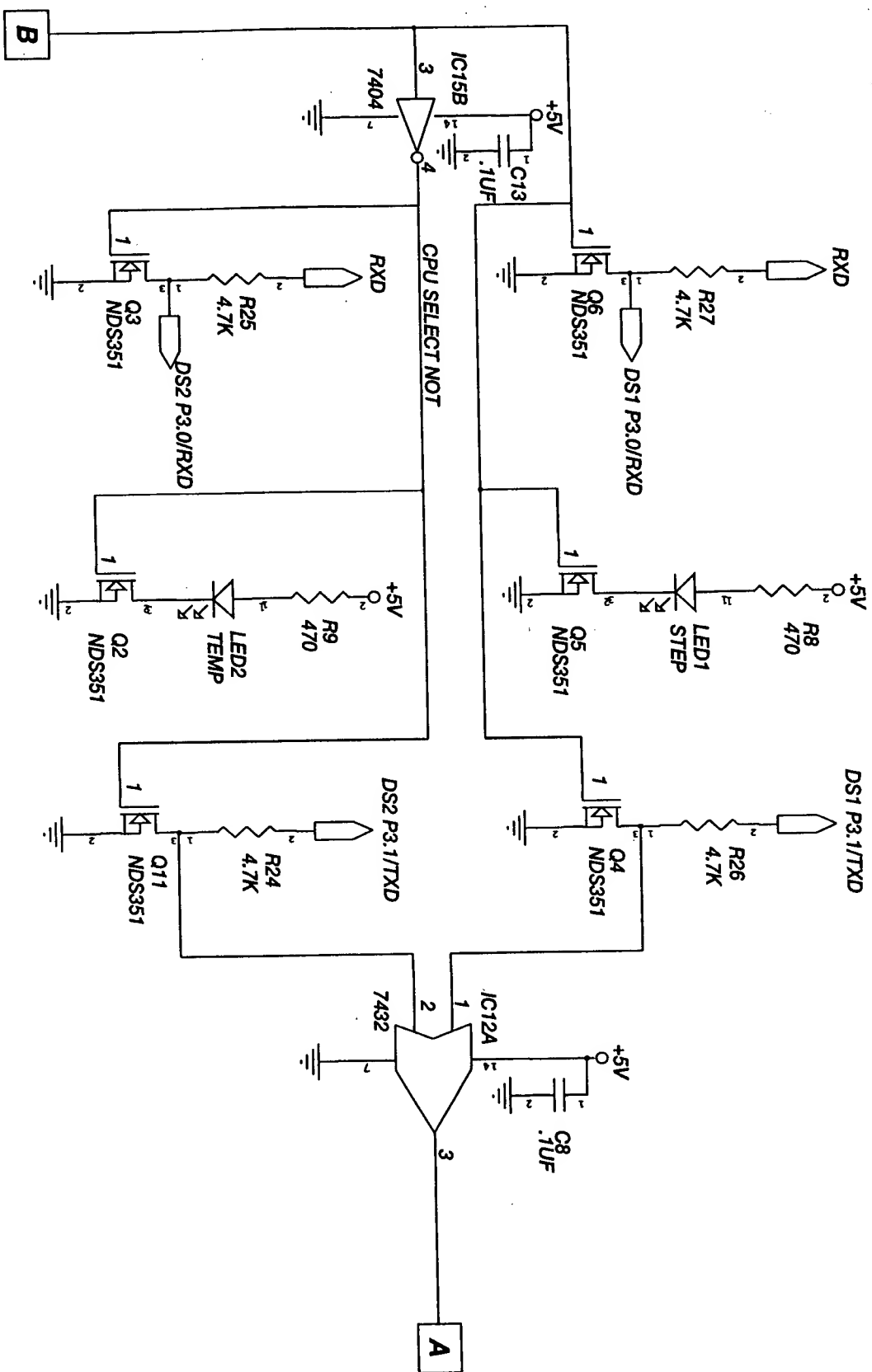


Fig. 30J





**Fig. 30L**

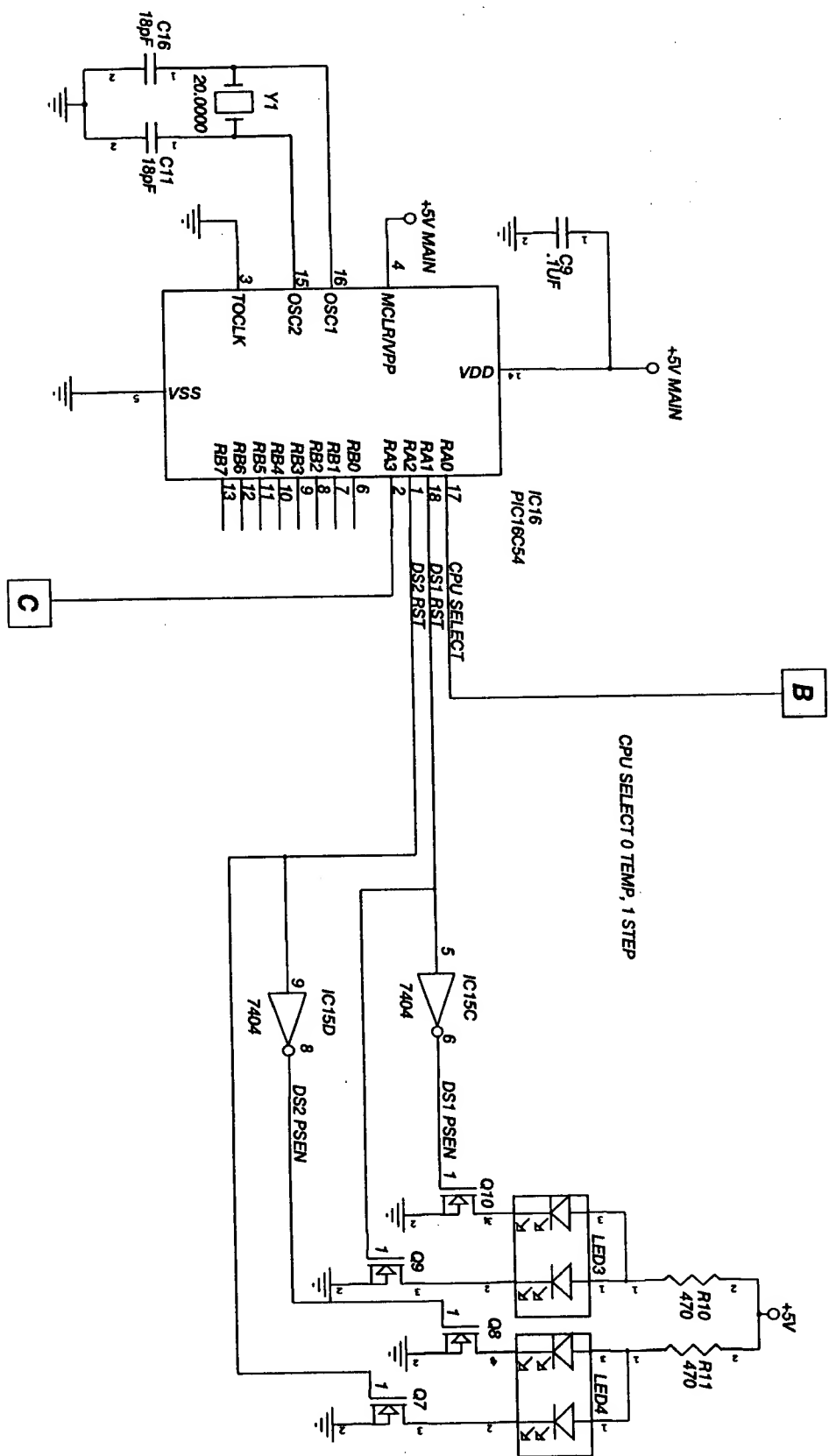
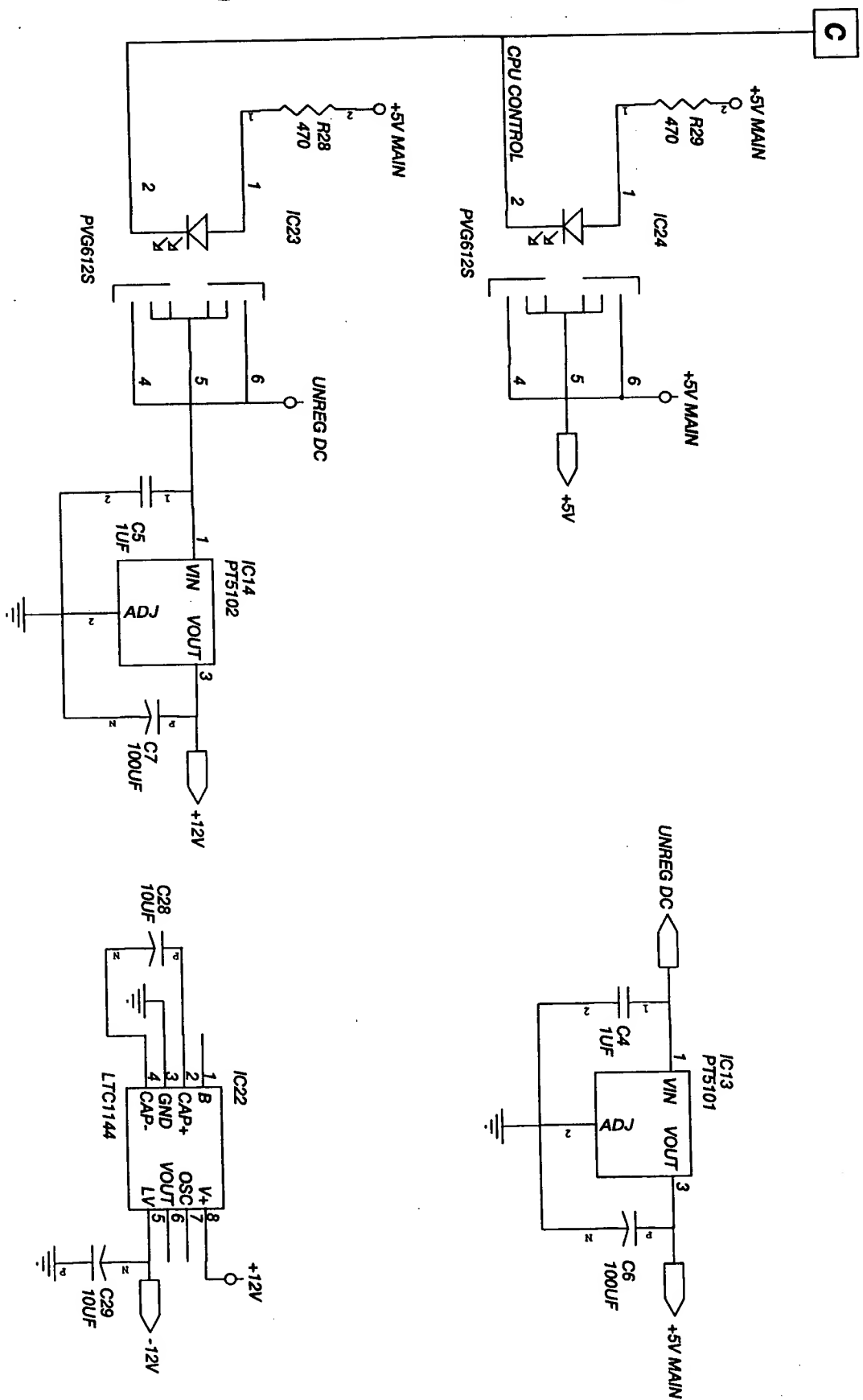


Fig. 30M







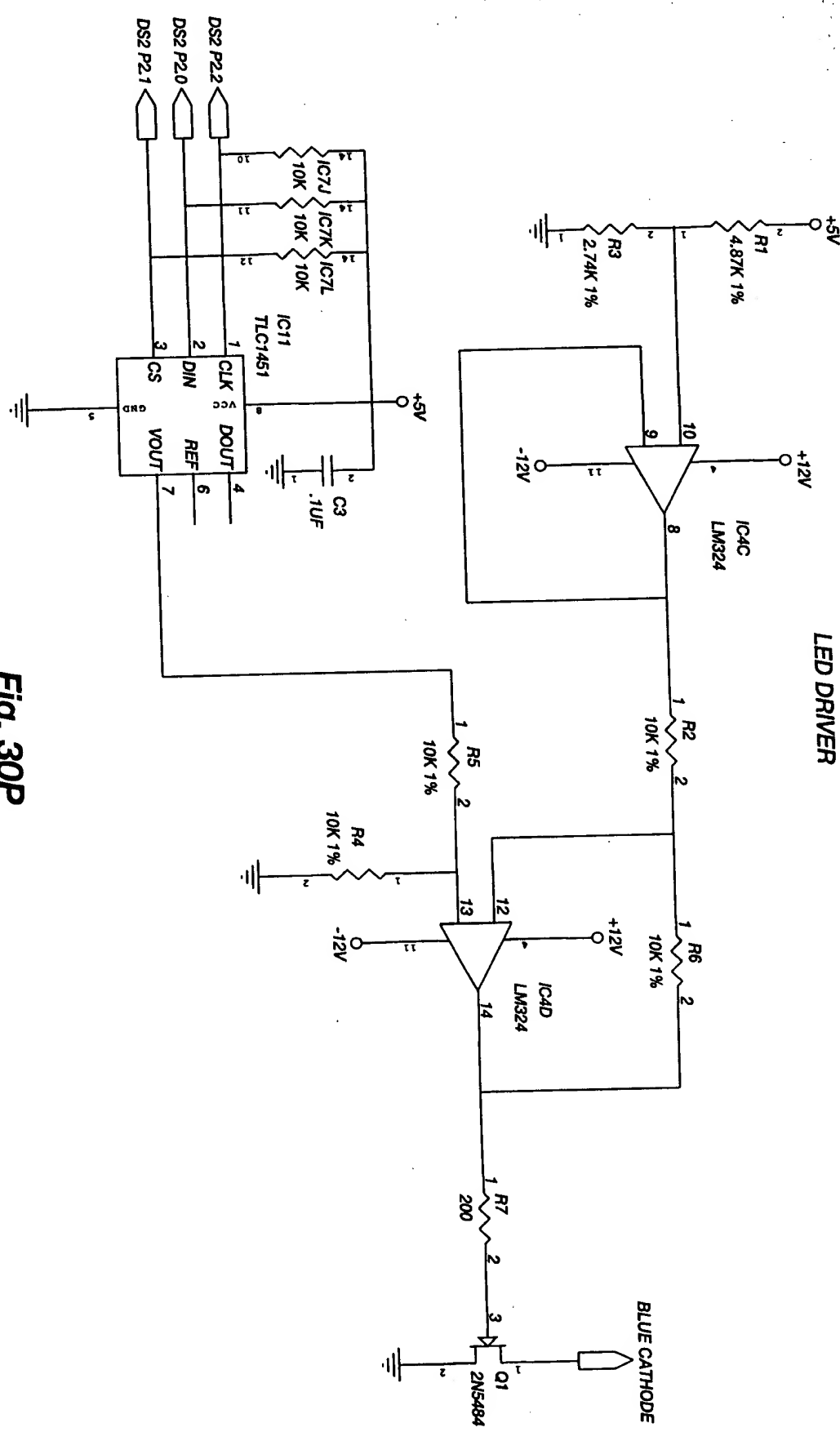
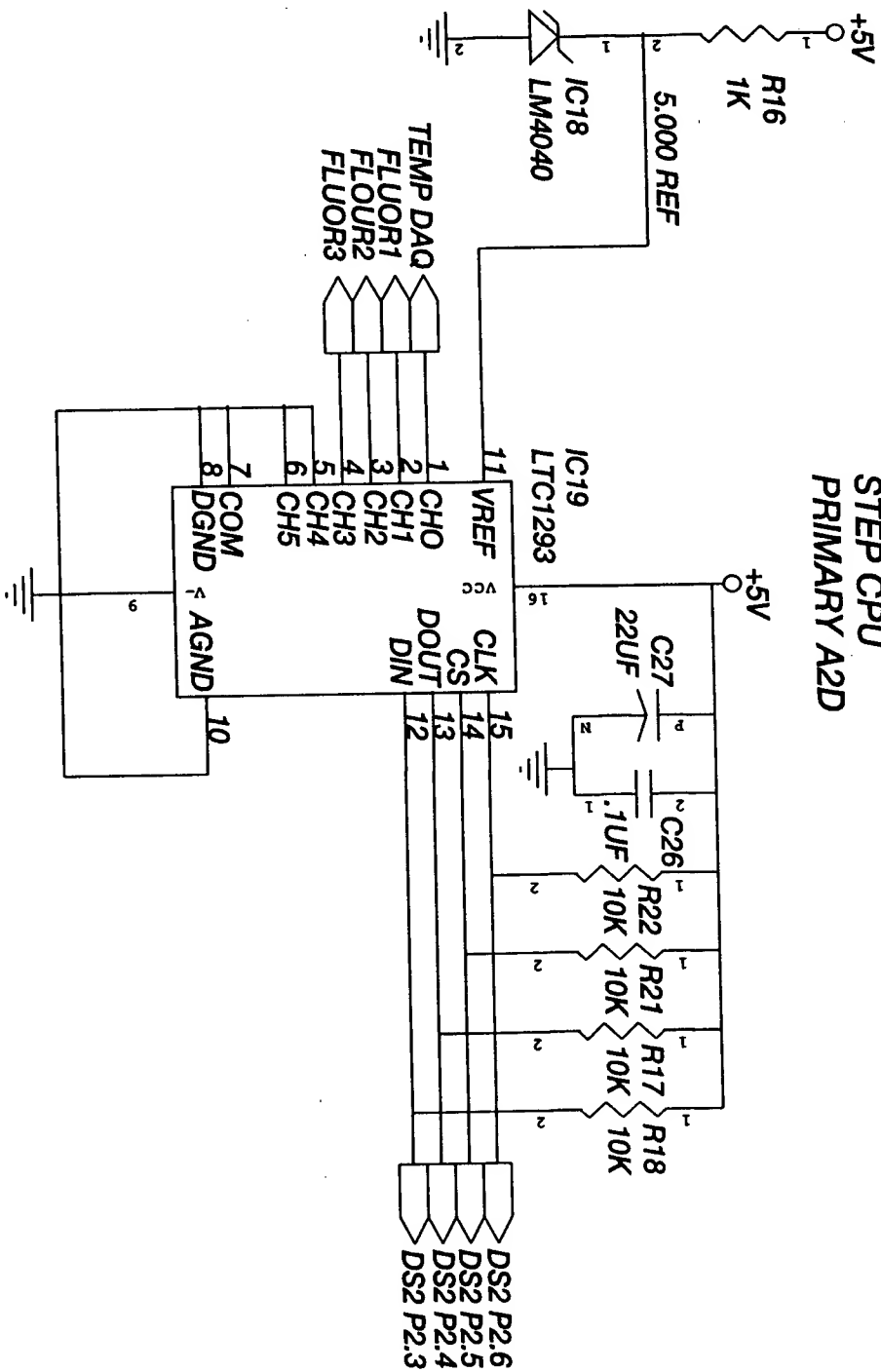


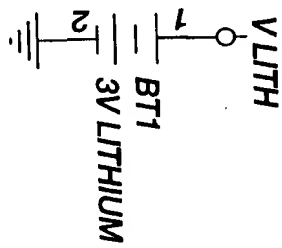
Fig. 30P

## STEP CPU PRIMARY A2D

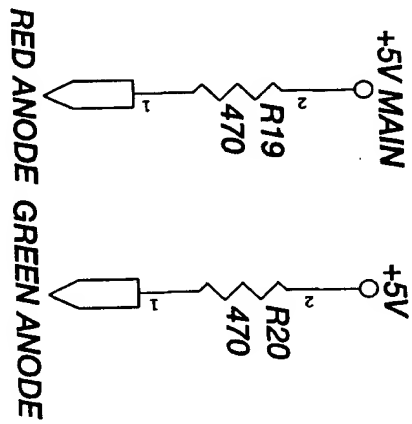


**Fig. 30Q**

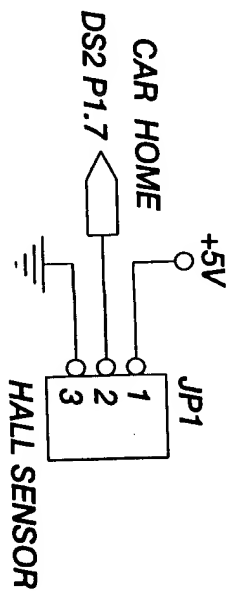
# STATIC MEMORY BATTERY



# INDICATORS



# HALL SENSOR



# MAIN BOARD FUSE

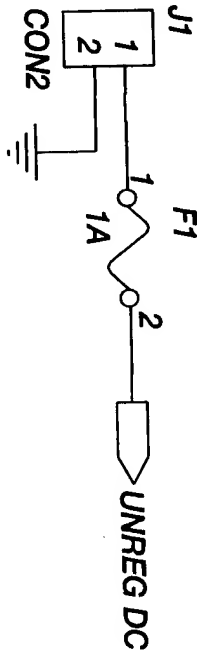


Fig. 30R

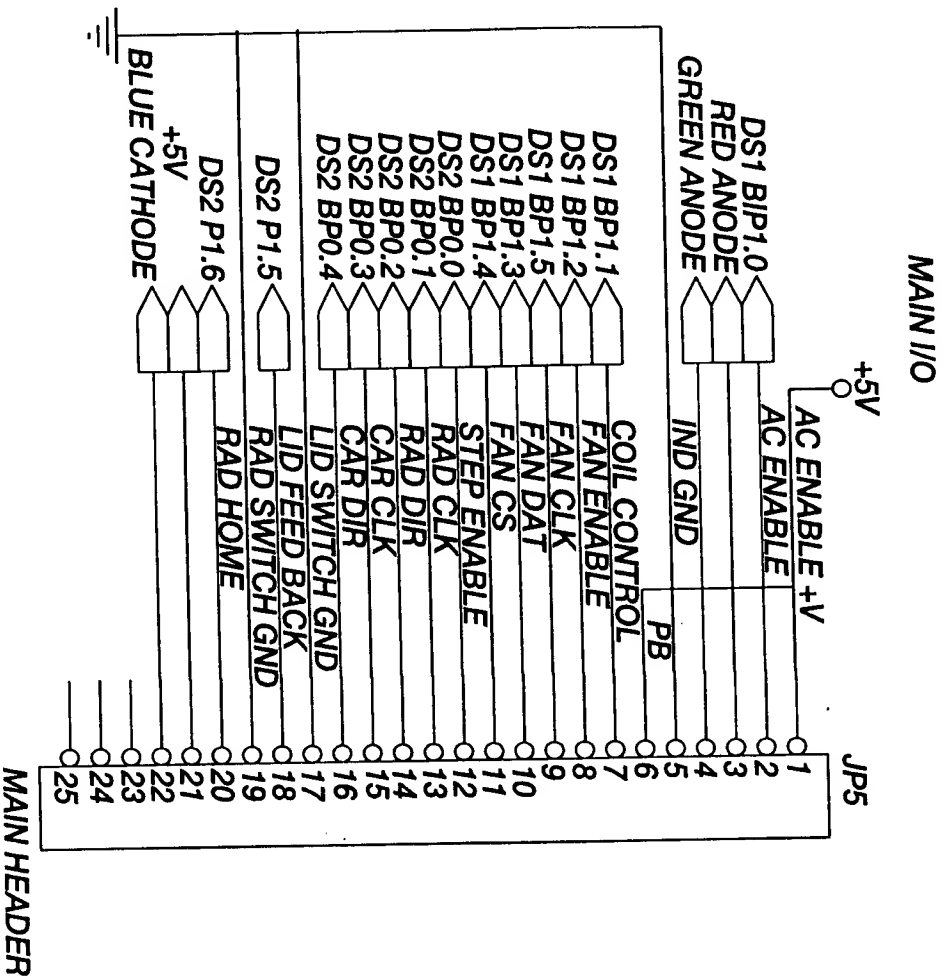


Fig. 30S

# FLUORIMETER I/O

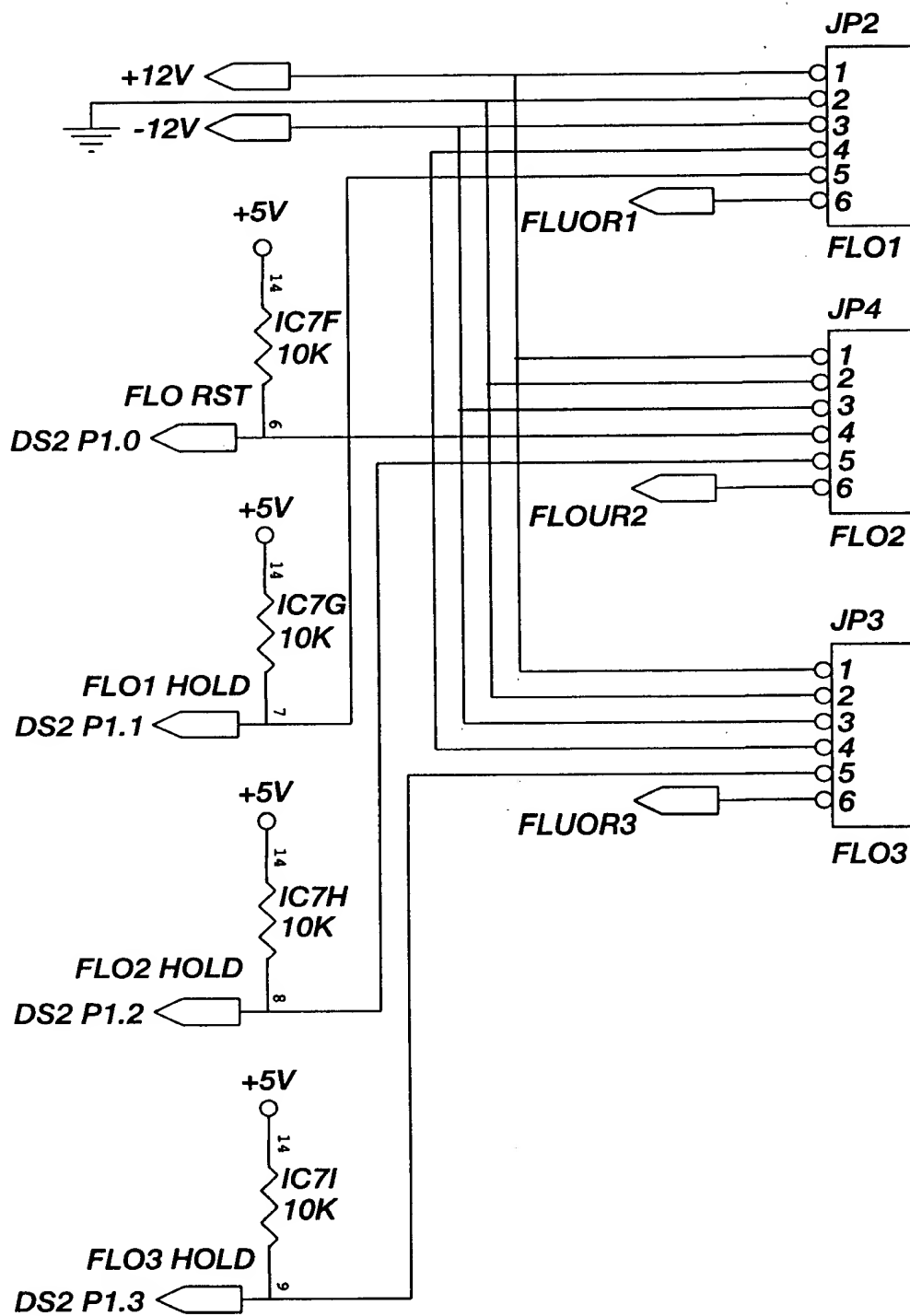
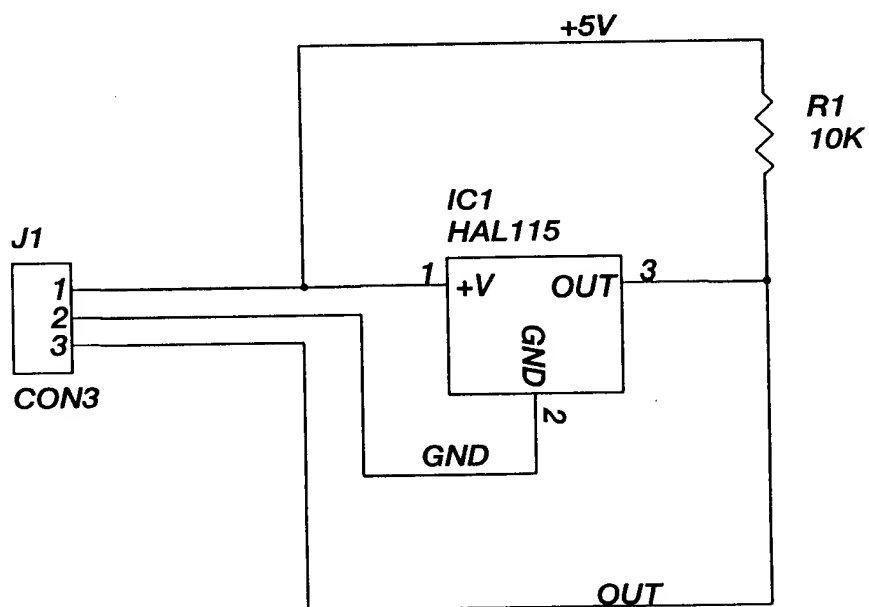


Fig. 30T



## CAROUSEL HOME POSITION SENSOR



**Fig. 30V**



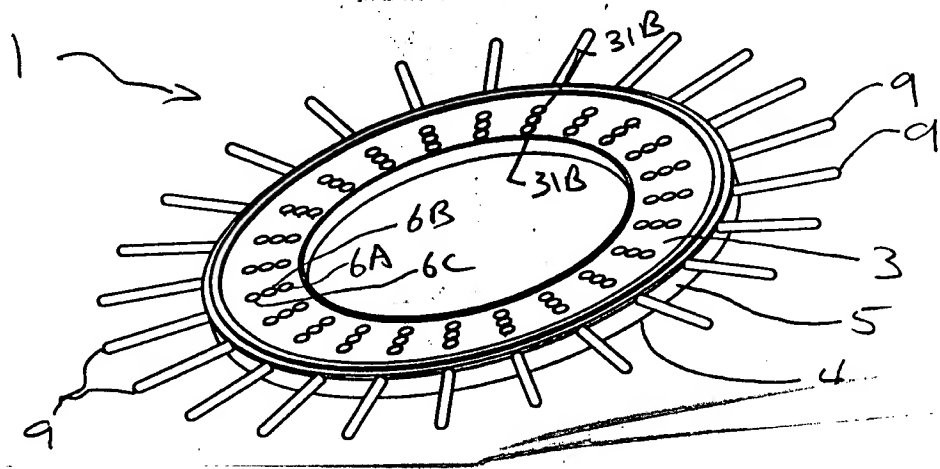


Fig. 31A

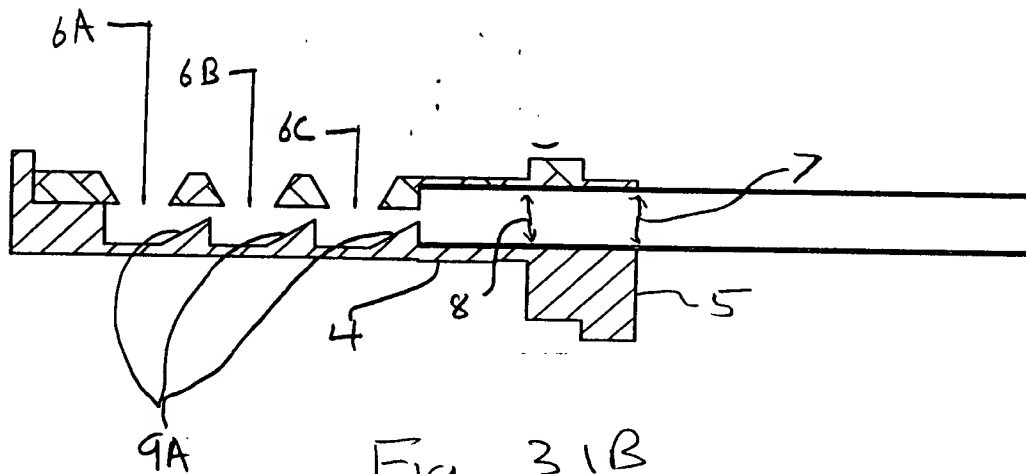


Fig. 31B

